

**UNITED STATES
SECURITIES AND EXCHANGE COMMISSION
WASHINGTON, D.C. 20549**

FORM 10-Q

QUARTERLY REPORT PURSUANT TO SECTION 13 OR 15(d) OF THE SECURITIES EXCHANGE ACT OF 1934

FOR THE QUARTERLY PERIOD ENDED SEPTEMBER 30, 2006

OR

TRANSITION REPORT PURSUANT TO SECTION 13 OR 15(d) OF THE SECURITIES EXCHANGE ACT OF 1934

FOR THE TRANSITION PERIOD FROM TO

Commission file number 000-18032

LATTICE SEMICONDUCTOR CORPORATION

(Exact name of Registrant as specified in its charter)

State of Delaware
(State or other jurisdiction of incorporation or organization)

93-0835214
(I.R.S. Employer Identification No.)

5555 N.E. Moore Court, Hillsboro, Oregon
(Address of principal executive offices)

97124-6421
(Zip Code)

(503) 268-8000

(Registrant's telephone number, including area code)

Indicate by check mark whether the Registrant (1) has filed all reports required to be filed by Section 13 or 15(d) of the Securities Exchange Act of 1934 during the preceding 12 months (or for such shorter period that the Registrant was required to file such reports), and (2) has been subject to such filing requirements for the past 90 days.

Yes No

Indicate by check mark whether the registrant is a large accelerated filer, an accelerated filer, or a non-accelerated filer. See definition of "accelerated filer and large accelerated filer" in Rule 12b-2 of the Exchange Act. (Check one):

Large accelerated filer

Accelerated filer

Non-accelerated filer

Indicate by check mark whether the registrant is a shell company (as defined in Rule 12b-2 of the Exchange Act).

Yes No

Number of shares of common stock outstanding as of November 3, 2006 114,503,103

The information contained in this Form 10-Q is as of November 7, 2006. This Form 10-Q should be read in conjunction with our Annual Report on Form 10-K for the year ended December 31, 2005.

LATTICE SEMICONDUCTOR CORPORATION

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Forward-Looking Statements

This Quarterly Report on Form 10-Q contains forward-looking statements within the meaning of Section 27A of the Securities Act of 1933, as amended, and Section 21E of the Securities Exchange Act of 1934, as amended. Any statements about our expectations, beliefs, plans, objectives, assumptions or future events or performance are not historical facts and may be forward-looking. We use words or phrases such as “anticipates,” “believes,” “estimates,” “expects,” “intends,” “plans,” “projects,” “may,” “will,” “should,” “continue,” “ongoing,” “future,” “potential” and similar words or phrases to identify forward-looking statements.

Forward-looking statements involve estimates, assumptions, risks and uncertainties that could cause actual results to differ materially from those expressed in the forward-looking statements. The key factors that could cause our actual results to differ materially from the forward-looking statements include overall semiconductor market conditions, market acceptance and demand for our new products, our dependencies on our silicon wafer suppliers, the impact of competitive products and pricing, technological and product development risks, and the other risks that are described herein and that are otherwise described from time to time in our filings with the Securities and Exchange Commission, including but not limited to, the items discussed in “Risk Factors” in Item 1A of Part II of this report. You should not unduly rely on forward-looking statements because our actual results could materially differ from those expressed in any forward-looking statements made by us. Further, any forward-looking statement applies only as of the date on which it is made. We are not required to update any forward-looking statement or statements to reflect events or circumstances after the date on which such statement is made or to reflect the occurrence of unanticipated events.

PART I. FINANCIAL INFORMATION

ITEM 1. FINANCIAL STATEMENTS

LATTICE SEMICONDUCTOR CORPORATION

CONDENSED CONSOLIDATED STATEMENT OF OPERATIONS

(In thousands, except per share data)

(unaudited)

	Three Months Ended		Nine Months Ended	
	September 30, 2006	September 30, 2005	September 30, 2006	September 30, 2005
Revenue	\$ 63,456	\$ 53,390	\$ 183,627	\$ 157,069
Costs and expenses:				
Cost of products sold	27,841	23,398	80,288	68,431
Research and development	21,473	23,159	63,718	73,395
Selling, general and administrative	14,480	13,558	40,902	44,324
Amortization of intangible assets	2,657	3,528	8,140	10,861
Restructuring charges	61	—	195	—
Total costs and expenses	66,512	63,643	193,243	197,011
Loss from operations	(3,056)	(10,253)	(9,616)	(39,942)
Other income, net	4,201	3,405	12,465	14,250
Income (loss) before income tax expense	1,145	(6,848)	2,849	(25,692)
Income tax expense	248	237	693	437
Net income (loss)	\$ 897	\$ (7,085)	\$ 2,156	\$ (26,129)

Basic net income (loss) per share	<u>\$ 0.01</u>	<u>\$ (0.06)</u>	<u>\$ 0.02</u>	<u>\$ (0.23)</u>
Diluted net income (loss) per share	<u>\$ 0.01</u>	<u>\$ (0.06)</u>	<u>\$ 0.02</u>	<u>\$ (0.23)</u>
Shares used in per share calculations:				
Basic	<u>114,376</u>	<u>113,544</u>	<u>114,091</u>	<u>113,495</u>
Diluted	<u>115,670</u>	<u>113,544</u>	<u>114,686</u>	<u>113,495</u>

See Accompanying Notes to Condensed Unaudited Consolidated Financial Statements.

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LATTICE SEMICONDUCTOR CORPORATION
CONDENSED CONSOLIDATED BALANCE SHEET
(In thousands, except share and par value data)
(unaudited)

	September 30, 2006	December 31, 2005
Assets		
Current assets:		
Cash and cash equivalents	\$ 47,639	\$ 39,336
Marketable securities	220,482	224,856
Accounts receivable, net	22,860	23,577
Inventories	36,544	28,581
Advances and other current assets	36,612	24,614
Total current assets	<u>364,137</u>	<u>340,964</u>
Foundry investments, advances and other assets	43,957	79,432
Property and equipment, net	47,258	45,450
Intangible assets, net	18,313	26,455
Goodwill	223,556	223,556
	<u>\$ 697,221</u>	<u>\$ 715,857</u>
Liabilities and Stockholders' Equity		
Current liabilities:		
Accounts payable and accrued expenses	\$ 51,269	\$ 52,121
Deferred income and allowances on sales to distributors	10,262	10,449
Total current liabilities	<u>61,531</u>	<u>62,570</u>
Zero Coupon Convertible Subordinated Notes	113,500	133,500
Other long-term liabilities	15,206	21,703
Commitments and contingencies	—	—
Stockholders' equity:		
Preferred stock, \$.01 par value, 10,000,000 shares authorized; none issued and outstanding	—	—
Common stock, \$.01 par value, 300,000,000 shares authorized; 114,496,000 and 113,646,000 shares issued and outstanding	1,145	1,136
Paid-in capital	601,979	595,145
Accumulated other comprehensive loss	(587)	(488)
Accumulated deficit	(95,553)	(97,709)
Total stockholders' equity	<u>506,984</u>	<u>498,084</u>
	<u>\$ 697,221</u>	<u>\$ 715,857</u>

See Accompanying Notes to Condensed Unaudited Consolidated Financial Statements.

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CONDENSED CONSOLIDATED STATEMENT OF CASH FLOWS
(In thousands)
(unaudited)

	Nine Months Ended	
	September 30, 2006	September 30, 2005
Cash flows from operating activities:		
Net income (loss)	\$ 2,156	\$ (26,129)
Adjustments to reconcile net income (loss) to net cash provided by (used in) operating activities:		
Depreciation and amortization	20,392	24,071
Gain on sale of UMC common stock	(1,313)	(4,291)
Gain on extinguishment of Zero Coupon Convertible Subordinated Notes	(1,985)	(4,927)
Stock-based compensation	2,462	1,634
Changes in assets and liabilities:		
Accounts receivable	717	(6,821)
Inventories	(7,963)	6,375
Foundry investments, advances and other assets	12,529	6,848
Accounts payable and accrued expenses	(9,681)	(19,847)
Deferred income and allowances on sales to distributors	(187)	1,556
Other liabilities	(45)	(1,876)
Total adjustments	14,926	2,722
Net cash provided by (used in) operating activities	17,082	(23,407)
Cash flows from investing activities:		
Proceeds from sales or maturities of marketable securities	195,145	216,967
Purchase of marketable securities	(190,771)	(182,913)
Proceeds from sale of equity securities (principally UMC common stock)	10,233	27,464
Capital expenditures	(10,943)	(7,958)
Net cash provided by investing activities	3,664	53,560
Cash flows from financing activities:		
Extinguishment of Zero Coupon Convertible Subordinated Notes	(17,810)	(30,182)
Advances on Yen line of credit	1,653	5,037
Pay down on Yen line of credit	(420)	(3,485)
Proceeds from issuance of common stock	4,134	9
Net cash used in financing activities	(12,443)	(28,621)
Net increase in cash and cash equivalents	8,303	1,532
Beginning cash and cash equivalents	39,336	44,816
Ending cash and cash equivalents	\$ 47,639	\$ 46,348
Supplemental disclosures of non-cash investing and financing activities:		
Unrealized gain on appreciation of foundry investments included in Accumulated other comprehensive loss	\$ 1,381	\$ 4,739
Distribution of assets held in deferred compensation plan	\$ 2,693	\$ 354

See Accompanying Notes to Condensed Unaudited Consolidated Financial Statements.

LATTICE SEMICONDUCTOR CORPORATION

NOTES TO CONDENSED CONSOLIDATED FINANCIAL STATEMENTS
(Unaudited)

Note 1 — Basis of Presentation:

The accompanying condensed consolidated financial statements are unaudited and have been prepared by Lattice Semiconductor Corporation (“the Company”) pursuant to the rules and regulations of the Securities and Exchange Commission and in our opinion include all adjustments, consisting only of normal recurring adjustments, necessary for the fair statement of results for the interim periods. Certain information and footnote disclosures normally included in financial statements prepared in accordance with generally accepted accounting principles have been condensed or omitted pursuant to such rules and regulations. These Condensed Consolidated Financial Statements should be read in conjunction with our audited financial statements and notes thereto included in our annual report on Form 10-K for the year ended December 31, 2005. Certain prior period disclosures have been reclassified to the extent required to be consistent with the current period presentation.

The preparation of financial statements in conformity with generally accepted accounting principles requires us to make estimates and assumptions that affect the reported amounts of assets and liabilities and disclosure of contingent liabilities at the date of the financial statements and the reported amount of revenue and expenses during the reporting periods. Actual results could differ from these estimates.

We report based on a 52 or 53-week year ending on the Saturday closest to December 31. For ease of presentation, we have adopted the convention of using March 31, June 30, September 30, and December 31, as period end dates for all financial statement captions.

New Accounting Pronouncements

In March 2006, the Emerging Issues Task Force reached a consensus on Issue No. 06-03 “How Taxes Collected from Customers and Remitted to Government Authorities Should Be Presented in the Income Statement (That Is, Gross versus Net Presentation)” (“EITF No. 06-03”). The Company is required to adopt the provisions of EITF No. 06-03 beginning its fiscal year 2007. The Company does not expect the provisions of EITF No. 06-03 to have a material impact on the Company’s consolidated financial position, results of operations or cash flows.

In June 2006, the Financial Accounting Standards Board (“FASB”) issued Interpretation (“Interpretation”) No. 48, “Accounting for Uncertainty in Income Taxes - an interpretation of FASB Statement No. 109.” This Interpretation prescribes a recognition threshold and measurement attribute for the financial statement recognition and measurement of a tax position taken or expected to be taken in a tax return, and provides guidance on derecognition, classification, interest and penalties, accounting in interim periods, disclosure, and transition. This Interpretation is effective for fiscal years beginning after December 15, 2006. We are currently assessing the impact of the Interpretation on our consolidated financial statements.

In September 2006, FASB issued SFAS No. 157, “Fair Value Measurements” (“SFAS No. 157”), which defines fair value of certain assets and liabilities, establishes a framework for measuring fair value, and expands disclosures about fair value measurements. This statement does not require any new fair value measurements, but may change current practice for certain entities. This statement is effective for financial statements issued for fiscal years beginning after November 15, 2007, and interim periods within those years. We are currently assessing the impact of SFAS No. 157 on our consolidated financial statements.

In September 2006, the U.S. Securities and Exchange Commission (SEC) issued Staff Accounting Bulletin No. 108, “Considering the Effects of Prior Year Misstatements when Quantifying Misstatements in Current Year Financial Statements” (“SAB 108”). SAB 108 addresses the diversity in practice of quantifying financial statement misstatements resulting in the potential build up of improper amounts on the balance sheet. SAB 108 provides interpretive guidance on how the effects of the carryover or reversal of prior year misstatements should be considered in quantifying a current year misstatement. The SEC staff believes that registrants should quantify errors using both a balance sheet and an income statement approach and evaluate whether either approach results in quantifying a misstatement that, when all relevant quantitative and qualitative factors are considered, is material. SAB 108 is effective for companies with fiscal years ending after November 15, 2006. SAB 108 allows a one-time transitional cumulative effect adjustment to beginning retained earnings, in the first year of adoption, for errors that were not previously deemed material, but are material under the guidance in SAB 108. We are currently assessing the impact of SAB 108 on our consolidated financial statements.

Note 2 — Revenue Recognition:

Revenue from sales to customers is recognized upon shipment provided that persuasive evidence of an arrangement exists, the price is fixed or determinable, title has transferred, collection of resulting receivables is probable, there are no customer acceptance requirements and no remaining significant obligations. Certain of our sales are made to distributors under agreements providing price protection and right of return on unsold merchandise. Revenue and cost relating to such distributor sales are deferred until either the product is sold by the distributor or return privileges and price protection rights terminate, at which time related estimated distributor resale revenue, estimated effects of distributor price adjustments, and estimated costs are reflected in income. Revenue from software sales was not material for the periods presented.

During the first quarter of 2006, we entered into business arrangements with certain distributors to issue accounts receivable credit adjustments (“distributor advances”) to reduce the distributors’ working capital required to service our end

customers. The distributor advances are for anticipated future price discounts and are recorded as a reduction of Deferred income and allowances on sales to distributors. These arrangements are unsecured, bear no interest, are settled on a quarterly basis and are due upon demand. The distributor advances have no impact on revenue recognition and totaled \$6.0 million at September 30, 2006.

Note 3 — Net Income (Loss) Per Share:

Net income (loss) per share is computed based on the weighted average number of shares of common stock and potentially dilutive securities assumed to be outstanding during the period using the treasury stock method. Potentially dilutive securities consist of stock options, warrants to purchase common stock and Zero Coupon Convertible Subordinated Notes due in 2010 (“Convertible Notes”).

A reconciliation of basic and diluted net income (loss) per share is presented below (in thousands, except for per share data):

	Three Months Ended		Nine Months Ended	
	September 30, 2006	September 30, 2005	September 30, 2006	September 30, 2005
Net income (loss)	\$ 897	\$ (7,085)	\$ 2,156	\$ (26,129)
Shares used in basic net income (loss) per share calculations	114,376	113,544	114,091	113,495
Dilutive effect of stock options and warrants	1,294	—	595	—
Shares used in diluted net income (loss) per share	115,670	113,544	114,686	113,495
Basic net income (loss) per share	\$ 0.01	\$ (0.06)	\$ 0.02	\$ (0.23)
Diluted net income (loss) per share	\$ 0.01	\$ (0.06)	\$ 0.02	\$ (0.23)

The computation of diluted earnings per share excludes the effects of stock options and warrants aggregating approximately 19.6 million shares for the three months ended September 30, 2006 and 20.7 million shares for the nine months ended September 30, 2006 because the effect was antidilutive. The computation of diluted earnings per share excludes the effects of stock options and warrants aggregating approximately 24.5 million shares for the three months ended September 30, 2005 and 24.2 million shares for the nine months ended September 30, 2005 because the effect was antidilutive. Stock options and warrants are antidilutive when the exercise price of the option or warrant is greater than the average market price for the period.

For all periods presented, the effects of Convertible Notes, aggregating approximately 9.8 million and 10.2 million shares for the three months and nine months ended September 30, 2006, respectively, and 11.5 million and 12.5 million shares for the three months and nine months ended September 30, 2005, respectively, are excluded in the computation of diluted earnings per share, as the contingent conversion features were not triggered for any of the periods presented.

Note 4 — Stock-Based Compensation:

Prior to January 1, 2006, the Company accounted for stock options in accordance with the provisions of Accounting Principles Board Opinion No. 25, "Accounting for Stock Issued to Employees" ("APB 25"), and related interpretations, and therefore related compensation expense was limited to the intrinsic value. Accordingly, the Company generally recognized stock-based compensation expense only when it granted options with an exercise price less than the fair value of the stock on the grant date. Any resulting compensation expense was recognized on an accelerated basis in accordance with FASB Interpretation No. 28, "Accounting for Stock Appreciation Rights and Other Variable Stock Option or Award Plans" ("FIN 28"), over the associated service period, which was generally the option vesting term.

Prior to January 1, 2006, the Company provided pro forma disclosure amounts in accordance with SFAS No. 148, "Accounting for Stock-Based Compensation - Transition and Disclosure" ("SFAS 148"), as if the fair value method defined by SFAS No. 123, "Accounting for Stock-Based Compensation" ("SFAS 123"), had been applied to its stock-based compensation.

Effective January 1, 2006, the Company began recording stock-based compensation expense related to employee and director stock options and the Employee Stock Purchase Plan ("ESPP") in accordance with "Share Based Payment — a revision of SFAS No. 123, Accounting for Stock-Based Compensation" ("SFAS 123(R)"), as interpreted by SEC Staff Accounting Bulletin No. 107 ("SAB 107").

The Company adopted the modified prospective transition method provided for under SFAS 123(R), and consequently has not retroactively adjusted results from prior periods. Under this transition method, compensation expense associated with stock options now includes: 1) quarterly amortization related to the remaining unvested portion of all stock option awards granted prior to January 1, 2006, based on the grant date fair value estimated in accordance with the original provisions of SFAS 123; and 2) quarterly amortization related to all stock option awards granted on or after January 1, 2006, based on the grant date fair value estimated in accordance with the provisions of SFAS 123(R). In addition, the Company records expense over the offering period in connection with shares issuable under the ESPP. To calculate the excess tax benefits available for use in offsetting future tax shortfalls as of the date of implementation, the Company followed the alternative transition method discussed in FASB Staff Position No. 123(R)-3. The compensation expense for employee, director and ESPP based compensation awards includes an estimate for forfeitures and is recognized over the requisite service period of the options or stock plan offering period, as applicable, using the straight-line method. As a result of the adoption of SFAS 123(R), the effect of recording stock-based compensation for the three and nine months ended September 30, 2006 was as follows:

	<u>Three Months Ended</u> <u>September 30, 2006</u>	<u>Nine Months Ended</u> <u>September 30, 2006</u>
	(In thousands, except per share data)	
Stock-based compensation expense included in net income by type of award:		
Employee and director stock options	\$ 947	\$ 2,162
Employee stock purchase plan	170	270
Related to acquisitions	—	30
Total stock-based compensation	\$ 1,117	\$ 2,462
Effect on earnings per share:		
Basic and diluted - as reported	\$ 0.01	\$ 0.02

The tax benefit and the resulting effect on cash flows from operations and financing activities related to stock-based compensation expense was not recognized as the Company currently provides a full valuation allowance for its deferred tax assets.

SFAS 123(R) also requires that the Company recognize compensation expense for only the portion of employee and director options and ESPP rights that are expected to vest. Therefore, the Company applies estimated forfeiture rates that are derived from historical employee termination behavior using a stratified model based on an employee's position within the Company. If the actual number of forfeitures differs from the number estimated by management, additional adjustments to compensation expense may be required in future periods.

Total stock-based compensation expense was included in the Condensed Consolidated Statement of Operations as follows (in thousands):

Line Item:	<u>Three Months Ended</u>		<u>Nine Months Ended</u>	
	<u>September 30,</u> <u>2006</u> <u>(SFAS 123(R))</u>	<u>September 30,</u> <u>2005</u> <u>(APB 25)</u>	<u>September 30,</u> <u>2006</u> <u>(SFAS 123(R))</u>	<u>September 30,</u> <u>2005</u> <u>(APB 25)</u>
Cost of products sold	\$ 105	\$ —	\$ 219	\$ —
Research and development	589	440	1,314	1,634
Selling, general and administrative	423	—	929	—
	\$ 1,117	\$ 440	\$ 2,462	\$ 1,634

The Company's employee stock option plans include principal plans adopted in 1996 and 2001 ("principal option plans"), as well as various stock option plans assumed through acquisitions under which stock options are outstanding. The principal option plans provide for grants of options to employees to purchase common stock at the fair market value of such

shares on the grant date. The options generally vest quarterly over a four-year period beginning on the grant date. Options granted under the principal option plans are generally non-qualified stock options but the principal option plans permit some options granted to qualify as "incentive stock options" under the U.S. Internal Revenue Code. The contractual term of options granted prior to January 31, 2006 is generally ten years, while the contractual term of options granted subsequent to January 31, 2006 is generally seven years.

The Company's director stock option plan, which was amended and approved most recently by its stockholders in May 2006, provides that non-employee members of our board of directors receive non-qualified option grants in set amounts and at set times, at option prices equal to the fair market value on the date of grant. Options granted following the May 2, 2006 amendment vest over a period of three years (for initial grants to new directors), or over a period of up to one year following the completion of vesting of all previously granted director options (for subsequent option grants). The contractual term of such options is ten years.

The Company's ESPP, which was amended and approved most recently by our stockholders in May 2004, permits eligible employees to purchase shares of common stock through payroll deductions, not to exceed 10% of an employee's compensation. The purchase price of the shares is the lower of 85% of the fair market value of the stock at the beginning of each six-month offering period or 85% of the fair market value at the end of such period, but in no event less than the book value per share at the mid-point of each offering period. The Company has treated the ESPP as a compensatory plan, and recorded compensation expense related to the ESPP of \$0.2 million and \$0.3 million in the three and nine months ended September 30, 2006, respectively.

The fair value of each option award is estimated on the date of grant using the Black-Scholes valuation model and the assumptions noted in the following table. Beginning January 1, 2006, we are estimating the expected term of stock options based on the simplified method provided for under SAB 107. The expected volatility of both stock options and ESPP shares is based on the daily historical volatility of our stock price, measured over the expected life of the option. The risk-free interest rate is based on the implied yield on a U.S. Treasury zero-coupon issue with a remaining term equal to the expected term of the option. The dividend yield reflects that the Company has not paid any cash dividends since inception and does not intend to pay any cash dividends in the foreseeable future.

	Grants for			
	Three Months Ended		Nine Months Ended	
	September 30, 2006	September 30, 2005	September 30, 2006	September 30, 2005
Employee and Director Stock Options:				
Expected volatility	58.1%	39.4%	58.1%	39.7%
Risk-free interest rate	4.9%	4.2%	4.9%	4.2%
Expected term (in years)	4.75	3.1	4.75	3.1
Dividend yield	0%	0%	0%	0%
Employee Stock Purchase Plan:				
Expected volatility	51.0%	32.2%	28.1%	32.2%
Risk-free interest rate	5.1%	4.3%	4.6%	4.2%
Expected term (in years)	0.5	0.5	0.5	0.5
Dividend yield	0%	0%	0%	0%

The Black-Scholes option pricing model was developed for use in estimating the fair value of freely tradable, fully transferable options without vesting restrictions. Our stock options have characteristics that differ significantly from those of freely tradable, fully transferable options. The Black-Scholes option pricing model also requires highly subjective assumptions, including expected stock price volatility and expected stock option term, which greatly affect the calculated fair value of an option. Our actual stock price volatility and option term may be materially different from the assumptions used herein.

As of September 30, 2006, there was \$10.0 million of total unrecognized compensation cost related to employee and director stock options and purchases related to the ESPP, which is expected to be recognized over a weighted average period of 3.1 years. The Company's current practice is to issue new shares to satisfy option exercises. In conjunction with the adoption of SFAS 123(R), the Company changed its method of recognizing the expense of stock-based compensation over the requisite service period from the accelerated approach in accordance with FIN 28 to the straight-line method. Compensation expense for all stock-based compensation awards granted on or prior to December 31, 2005 will continue to be recognized using the accelerated approach, while compensation expense for all stock-based compensation awards granted subsequent to December 31, 2005 will be recognized using the straight-line method.

A summary of stock option activity under the plans for the nine months ended September 30, 2006 is presented as follows:

	Shares (In thousands)	Weighted Average Exercise Price	Weighted Average Remaining Contractual Term (Years)	Aggregate Intrinsic Value (In thousands)
Balance, January 1, 2006	23,926	\$ 7.64		

Granted	3,763	5.67		
Exercised	(611)	5.03		
Forfeited or expired	(5,481)	8.66		
Balance, September 30, 2006	21,597	\$ 7.11	6.20	\$ 18,385
Vested and expected to vest at September 30, 2006	18,864	\$ 7.39	5.85	\$ 13,848
Exercisable, September 30, 2006	14,343	\$ 8.13	5.40	\$ 5,850

The aggregate intrinsic value in the table above represents the total pretax intrinsic value (the difference between the Company's closing stock price on the last trading day of the third quarter of fiscal 2006 and the exercise price, multiplied by the number of in-the-money options) that would have been received by the option holders had all option holders exercised their options on September 30, 2006. This amount changes based on the fair market value of the Company's stock. Total intrinsic value of options exercised for the nine months ended September 30, 2006 was \$0.8 million.

The resultant grant date weighted-average fair values calculated using the Black-Scholes option pricing model and the noted assumptions for stock options granted were \$3.00 and \$1.49 for the third quarter of 2006 and 2005, respectively, and \$3.00 and \$1.50 for the first nine months of 2006 and 2005, respectively. The weighted average fair values calculated using the Black-Scholes option pricing model for the ESPP were \$1.83 and \$0.45 for the third quarter of 2006 and 2005, respectively, and \$0.94 and \$0.45 for the first nine months of 2006 and 2005, respectively.

As of September 30, 2006, a total of 4.3 million shares of our common stock were available for future grants under our stock option plans. Shares subject to stock option grants that expire or are cancelled without delivery of such shares generally become available for reissuance under these plans. As of September 30, 2006, a total of 0.5 million shares of our common stock were available for future purchases under our ESPP.

The table below sets out the pro forma amounts of net loss and net loss per share that would have resulted for the nine months ended September 30, 2005, if the Company had accounted for its employee stock plans under the fair value recognition provisions of SFAS No.123. These pro forma effects may not be representative of expense in future periods since the estimated fair value of stock options on the date of grant is amortized to expense over the vesting period and additional options may be granted or options may be cancelled in future years:

	<u>Nine Months Ended</u> <u>September 30, 2005</u> <u>(In thousands)</u>
Net loss as reported	\$ (26,129)
Add: Stock compensation expense related to acquisitions (attributable to research and development activities) included in reported net loss, net of related tax effects	1,634
Deduct: Total stock-based employee compensation expense, net of estimated forfeitures, determined under fair value based method for all awards, net of related tax effects	(6,063)
Pro forma net loss	<u>\$ (30,558)</u>
Net loss per share:	
Basic and diluted - as reported	\$ (0.23)
Basic and diluted - pro forma	\$ (0.27)

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Upon the adoption of SFAS 123(R) effective January 1, 2006, stock compensation expense related to acquisitions (attributable to research and development activities) and previously classified as part of Amortization of intangible assets has been reclassified to Research and development expense. Such deferred stock compensation attributable to research and development activities was completely recognized as of March 31, 2006. Compensation expense was less than \$0.1 million for both the three and nine months ended September 30, 2006 compared to \$0.4 million and \$1.6 million for the three and nine months ended September 30, 2005, respectively.

Note 5 — Inventories (in thousands):

	<u>September 30,</u> <u>2006</u>	<u>December 31,</u> <u>2005</u>
Work in progress	\$ 25,522	\$ 20,348
Finished goods	11,022	8,233
	<u>\$ 36,544</u>	<u>\$ 28,581</u>

Note 6 — Changes in Stockholders' Equity (in thousands):

	<u>Common</u> <u>stock</u>	<u>Paid-in</u> <u>capital</u>	<u>Accumulated</u> <u>other</u> <u>comprehensive loss</u>	<u>Accumulated</u> <u>deficit</u>	<u>Total</u>
Balances, December 31, 2005	\$ 1,136	\$ 595,145	\$ (488)	\$ (97,709)	\$ 498,084
Common stock issued in connection with exercise of stock options and ESPP	9	4,125	—	—	4,134
Unrealized gain on foundry investments, net (Note 7)	—	—	1,381	—	1,381
Previously unrealized gain on foundry investments sold (Note 7)	—	—	(1,435)	—	(1,435)

Stock-based compensation expense related to employee and director stock options and ESPP	—	2,432	—	—	2,432
Distribution of stock held in deferred compensation plan	—	247	—	—	247
Stock compensation expense related to acquisitions	—	30	—	—	30
Translation adjustment	—	—	(45)	—	(45)
Net income for the nine month period	—	—	—	2,156	2,156
Balances, September 30, 2006	\$ 1,145	\$ 601,979	\$ (587)	\$ (95,553)	\$ 506,984

Total comprehensive income for the first nine months of 2006 was approximately \$2.1 million and is substantially comprised of net income of \$2.2 million along with an unrealized gain on foundry investments of \$1.4 million, and a \$1.4 million gain on sale of foundry investments previously included in Accumulated other comprehensive loss.

Note 7 — Foundry Investments, Advances and Other Assets (in thousands):

	September 30, 2006	December 31, 2005
Foundry investments and other assets	\$ 36,224	\$ 42,740
Wafer supply advances	28,375	50,427
	64,599	93,167
Less: UMC common stock available for sale	(57)	(1,356)
Current portion of wafer advance	(20,585)	(12,379)
	\$ 43,957	\$ 79,432

On September 10, 2004, we entered into an Advance Payment and Purchase Agreement (the “Fujitsu APP Agreement”) with Fujitsu Limited (“Fujitsu”), pursuant to which we will advance \$125.0 million to Fujitsu. The initial two payments of \$25.0 million each were made in October 2004 and January 2005. The remaining payments will be made in two stages upon the achievement of certain milestones and will be recorded upon completion of the related milestone. The third milestone has been achieved, and as a result, we will pay \$37.5 million in the fourth quarter of 2006. We currently anticipate that the final milestone will be met in the fourth quarter of 2006 and we will pay the final \$37.5 million in the first quarter of 2007.

During the third quarter of 2006, we entered into an amendment (“Amendment”) to the Fujitsu APP Agreement. Prior to the Amendment our \$125.0 million advance was to be credited against the purchase price of 300mm wafers received from Fujitsu. The Amendment permits us to also credit the advance against the purchase price of 200mm wafers. The Fujitsu APP Agreement will continue until the full amount of the advance payment has been returned to us in the form of wafer credits or other repayment, subject to the right of either party to terminate the agreement upon the occurrence of certain events. Prior to the Amendment we could request a refund of the unused amount of the advance payment if we have not used all of our wafer credits by December 31, 2007. Pursuant to the Amendment, we may request a refund of the unused amount of the advance payment if we have not used all of our wafer credits by December 31, 2008. The repayment obligation of Fujitsu is unsecured.

During the nine months ended September 30, 2006, we sold approximately 15.7 million shares of UMC common stock for approximately \$10.2 million in cash, resulting in a net gain of approximately \$1.3 million. If we further liquidate our UMC common stock, it is likely that the amount of any future realized gain or loss will be different from the accounting gain or loss reported in prior periods.

Note 8 — Purchased Intangible Assets:

The following tables present details of the Company’s total purchased intangible assets (in millions):

September 30, 2006	Gross	Accumulated amortization	Net
Current technology	\$ 273.6	\$ (260.9)	\$ 12.7
Core technology	7.3	(6.0)	1.3
Licenses	10.2	(6.9)	3.3
Non-compete agreements	14.2	(14.1)	0.1
Workforce	4.7	(3.8)	0.9
Backlog	1.4	(1.4)	—
Customer list	17.4	(17.4)	—
Patents and trademarks	26.8	(26.8)	—
Total	\$ 355.6	\$ (337.3)	\$ 18.3

December 31, 2005	Gross	Accumulated amortization	Net
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Current technology	\$ 273.6	\$ (255.7)	\$ 17.9
Core technology	7.3	(4.8)	2.5
Licenses	10.2	(5.8)	4.4
Non-compete agreements	14.2	(14.1)	0.1
Workforce	4.7	(3.1)	1.6
Backlog	1.4	(1.4)	—
Customer list	17.4	(17.4)	—
Patents and trademarks	26.8	(26.8)	—
Total	\$ 355.6	\$ (329.1)	\$ 26.5

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The estimated future amortization expense of purchased intangible assets as of September 30, 2006 is as follows (in millions):

<u>Fiscal Year:</u>	<u>Amount</u>
2006 (remaining three months)	\$ 2.7
2007	9.8
2008	5.6
2009	0.2
	<u>\$ 18.3</u>

Note 9 — Restructuring Charges:

During the fourth quarter of 2005, we initiated and completed a restructuring plan implemented to reduce operating expenses. The restructuring encompassed three major components — a streamlining of research and development sites, a voluntary separation program for certain employees and an organizational consolidation within the Company's largest design center. These actions did not significantly impact the Company's product direction or product roll-out strategy.

In the fourth quarter of 2005, the Company recorded an initial restructuring charge of \$11.9 million. The charge primarily related to separation packages, costs to vacate space under long-term lease arrangements and the write-off of an intellectual property license. All of our restructuring accruals as of September 30, 2006 are related to the fourth quarter 2005 restructuring plan.

The following table displays the current estimate for each major type of cost associated with the restructuring (in thousands):

	<u>Accrual at 12/31/2005</u>	<u>Charged to Expense</u>	<u>Paid or Settled</u>	<u>Adjustments to Reserve</u>	<u>Accrual at 9/30/2006</u>
Severance and related costs	\$ 4,924	\$ 75	\$ (4,891)	\$ —	\$ 108
Lease loss reserve	2,167	152	(385)	(142)	1,792
Other	240	110	(338)	—	12
Total	<u>\$ 7,331</u>	<u>\$ 337</u>	<u>\$ (5,614)</u>	<u>\$ (142)</u>	<u>\$ 1,912</u>

Included in the above amounts are disposals of leasehold improvements and fixed assets totaling \$0.3 million and \$0.2 million, respectively. In the second quarter of 2006 the Company adjusted the lease loss reserve upon negotiating a sublease for its Colorado facility. In the second quarter of 2006, we also negotiated a conditional lease buyout agreement for the Company's facility in the United Kingdom; however, in the third quarter of 2006 the conditions to the closing of the buyout were not completed, and the agreement was cancelled. As a result, the lease reserve adjustment recorded in the second quarter for the United Kingdom was reversed in the third quarter. The above restructuring charges are based on estimates that are subject to change. Lease charges change based on our ability to either generate sublease income or terminate lease obligations at the amounts previously estimated, and are dependent upon lease market conditions at the time we negotiate the potential lease arrangements. Variance from these estimates could alter our ability to achieve anticipated expense reductions in the planned timeframe and modify our expected cash outflows and working capital.

At September 30, 2006, \$1.1 million of the lease loss reserve is classified as long-term, as it relates to operating lease commitments accrued as part of restructuring costs, payable after twelve months. \$1.3 million of the restructuring accrual at December 31, 2005 has been reclassified from Accounts payable and other accrued liabilities to Other long-term liabilities.

Note 10 — Legal Matters:

In September and October 2004, three putative class action complaints were filed in the United States District Court for the District of Oregon against Lattice Semiconductor Corporation, our Chief Executive Officer and President Stephen A.

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Skaggs, and our former Chief Executive Officer Cyrus Y. Tsui. These complaints were filed on behalf of a putative class of investors who purchased our stock between April 22, 2003 and April 19, 2004. They generally alleged violations of federal securities laws arising out of our previously announced restatement of financial results for the first, second and third quarters of 2003. Consistent with the usual procedures for cases of this kind, these cases were amended and consolidated into a single action. In an amended and consolidated complaint filed January 27, 2005 our former President and our former Controller were

added as defendants. The complaints generally sought an unspecified amount of damages, as well as attorney fees and costs. The Company has entered into an agreement with the plaintiffs to settle the consolidated action. The agreement, which has preliminary court approval, does not contain any admission of fault or wrongdoing on the part of the Company or any of the individual defendants in the litigation. The agreement is subject to, and conditioned upon, final court approval. The agreement provides that plaintiffs will receive an aggregate amount of \$3.5 million, inclusive of fees and expenses of counsel, in exchange for a release of the Company and the individual defendants from all claims asserted in the litigation. Subject to final approval by the Court, the Company's insurance carriers have agreed with the Company to pay the entire amount, on behalf of the Company, to settle the suit under the terms of the Company's director and officer liability insurance policy. The Company has recorded a liability in its financial statements for the proposed amount of the settlement. Additionally, because the insurance carriers have agreed to pay the entire \$3.5 million settlement amount, a receivable was also recorded for the same amount. Accordingly, there is no impact to the Condensed Consolidated Statement of Operations as the amounts of the settlement and the insurance recovery fully offset each other.

On June 14, 2005, we announced that our Audit Committee, in connection with its responsibilities for financial oversight, was conducting an internal examination. The examination concerned issues primarily associated with executive compensation and several items pertaining to our internal controls. On August 9, 2005, we announced that the Audit Committee had concluded the examination. We have furnished information regarding the matters examined by the Audit Committee to the Securities and Exchange Commission, which has been conducting an informal inquiry into our prior restatement of financial results. On September 30, 2005 the Securities and Exchange Commission issued a Cease and Desist Order concerning our former Controller, and referenced certain events in connection with our prior restatement of financial results. We are cooperating fully with the Securities and Exchange Commission and intend to continue to do so. In September 2006, we made a settlement offer to the Securities and Exchange Commission to resolve the informal inquiry. We offered to consent to the entry of a cease and desist order with respect to violations of Sections 13(a) and 13(b)(2)(A)-(B) of the Securities Exchange Act of 1934, as amended, and Rule 13a-13 promulgated thereunder. We will not be required to pay any penalties in connection with the settlement. The proposed settlement is subject to final approval by the Securities and Exchange Commission. The Securities and Exchange Commission staff has agreed to recommend such approval. However, if we do not reach agreement with the Securities and Exchange Commission to settle its informal inquiry or if the Securities and Exchange Commission decides to pursue enforcement action against us, or any other governmental agency or regulatory body takes similar action, our management could be distracted, we could incur substantial costs and there could be a material adverse effect on our business.

We are exposed to certain asserted and unasserted potential claims. There can be no assurance that, with respect to potential claims made against us, we could resolve such claims under terms and conditions that would not have a material adverse effect on our business, our liquidity, our financial position or our operating results.

Note 11 — Segment and Geographic Information:

We operate in one industry segment comprising the design, development, manufacture and marketing of high performance programmable logic devices. Our revenue by major geographic area was as follows (in thousands):

	Three Months Ended		Nine Months Ended	
	September 30, 2006	September 30, 2005	September 30, 2006	September 30, 2005
United States revenue:	\$ 11,884	\$ 13,044	\$ 39,087	\$ 37,365
Export revenue:				
Europe	13,579	12,663	43,991	37,714
China	11,894	7,251	29,336	20,297
Japan	8,032	7,878	22,948	22,664
Taiwan	7,379	3,985	17,870	11,838
Other Asia	6,033	5,136	16,860	16,236
Other North America	4,655	3,433	13,535	10,955
Total export revenue	51,572	40,346	144,540	119,704
Total revenue	\$ 63,456	\$ 53,390	\$ 183,627	\$ 157,069

Resale of product through our two largest distributors accounted for approximately 12% and 9% of revenue in the first nine months of 2006, and 13% and 9% for the first nine months of 2005. More than 90% of our property and equipment is located in the United States.

Note 12 — Income Taxes:

The Internal Revenue Service has examined our income tax returns for 2001 and 2002, and has issued proposed adjustments that would result in payment by the Company of \$1.4 million, plus interest. These adjustments relate to the treatment of acquisition costs and a tax accounting method change for prepaid expenses. We do not agree with these proposed adjustments and are appealing, with the first IRS appeal hearing scheduled for November 14, 2006. Although the final resolution of this appeal and other tax issues is uncertain, we believe that adequate amounts have been provided for in the Condensed Consolidated Financial Statements. There is the possibility of either a favorable or an unfavorable impact on our results of operations in the period in which these matters are ultimately resolved, or in the period in which our estimates of the outcomes change.

Note 13 — Subsequent Event:

As described in Note 7, we have agreed with Fujitsu to advance a total of \$125.0 million to Fujitsu as prepayment for future wafer purchases. To date we have made payments totaling \$50.0 million, and our obligation to make the remaining advance payments for \$37.5 million each is conditioned on the achievement of specific milestones in the development and implementation of wafer manufacturing processes that will be used to manufacture our products. On November 2, 2006, the milestone was reached on which the first of the two remaining payments was conditioned, and thus we will be making a payment to Fujitsu of \$37.5 million within thirty days of November 2, 2006.

Lattice Semiconductor Corporation designs, develops and markets high performance programmable logic products and related software. Programmable logic devices are widely used semiconductor components that can be configured by the end customer as specific logic circuits, and enable the end customer to shorten design cycle times and reduce development costs. Our end customers are primarily original equipment manufacturers in the communications, computing, industrial, automotive, medical, consumer and military end markets.

Results of operations (overview of three month and nine month periods ended September 30, 2006):

Revenue

Revenue in the third quarter ended September 30, 2006 increased by \$10.1 million, or 19%, to \$63.5 million compared to \$53.4 million reported for the third quarter of 2005. Revenue for the nine months ended September 30, 2006 increased by \$26.6 million, or 17%, to \$183.6 million compared to \$157.1 million for the nine months ended September 30, 2005. The revenue increase is attributable to growth in New*, Mainstream* and Mature* products, which increased 121%, 13% and 2%, respectively, compared to the third quarter of 2005, and 89%, 13% and 5%, respectively, compared to the first nine months of 2005. The Company also experienced increased revenue from increases in distribution resale of 22% and 21% for the third quarter and nine months ended September 30, 2006, respectively, compared to the corresponding periods in 2005, and to a lesser extent, lower distributor price adjustments against distribution resale. Our future revenue growth is dependent upon, among other things, favorable market acceptance of our New* products.

* Product Classification:

New:	LatticeSC, LatticeECP2/M, LatticeECP, LatticeXP, MachXO, FPSC, ispXPLD, ispGDX2, Power Manager, ispClock
Mainstream:	ispMACH 4000/Z, ispXPGA, ispGDX/V, ispMACH 4/LV, ispLSI 2000V, ispLSI 5000V, ispMACH 5000VG, Software and IP
Mature:	ORCA 2, ORCA 3, ORCA 4, ispPAC, ispLSI 8000V, ispMACH 5000B, ispMACH 2LV, ispMACH 5LV, All 5-Volt CPLDs, all SPLDs

Revenue attributable to PLD and FPGA products increased 16% and 30%, respectively, for the third quarter of 2006 compared to the third quarter of 2005. Likewise, PLD and FPGA product revenue increased 15% and 27%, respectively, for the first nine months of 2006 compared to the first nine months of 2005. The composition of our revenue by product classification for the third quarter and first nine months of 2006 and 2005 was as follows:

	Three Months Ended		Nine Months Ended	
	September 30, 2006	September 30, 2005	September 30, 2006	September 30, 2005
New	17%	9%	14%	8%
Mainstream	50%	53%	50%	52%
Mature	33%	38%	36%	40%

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	Three Months Ended		Nine Months Ended	
	September 30, 2006	September 30, 2005	September 30, 2006	September 30, 2005
PLD	79%	81%	80%	81%
FPGA	21%	19%	20%	19%

Beginning with the quarter ended March 31, 2006 we have reclassified our New, Mainstream and Mature product categories to better reflect our current product portfolio. The New product category has been narrowed, and as such several products have been removed from our New product category and are now classified as Mainstream. As part of the change to product categories, we also reclassified certain products from Mainstream to Mature. Prior period ratios have been recalculated to reflect these new product category classifications. Newly released product families have also been added to the New product classification.

The composition of our revenue by geographical location of our direct and indirect customers is as follows (in thousands):

	Three Months Ended			Three Months Ended	
	September 30, 2006	September 30, 2005		September 30, 2006	September 30, 2005
United States revenue:	\$ 11,884	19%	\$ 13,044	24%	
Export revenue:					
Europe	13,579	21%	12,663	24%	
China	11,894	19%	7,251	14%	
Japan	8,032	13%	7,878	15%	
Taiwan	7,379	12%	3,985	7%	
Other Asia	6,033	9%	5,136	10%	
Other North America	4,655	7%	3,433	6%	
Total export revenue	51,572	81%	40,346	76%	
Total revenue	\$ 63,456	100%	\$ 53,390	100%	

	Nine Months Ended			Nine Months Ended	
	September 30, 2006	September 30, 2005		September 30, 2006	September 30, 2005
United States revenue:	\$ 39,087	22%	\$ 37,365	24%	

Export revenue:

Europe	43,991	24%	37,714	24%
China	29,336	16%	20,297	13%
Japan	22,948	12%	22,664	14%
Taiwan	17,870	10%	11,838	8%
Other Asia	16,860	9%	16,236	10%
Other North America	13,535	7%	10,955	7%
Total export revenue	144,540	78%	119,704	76%
Total revenue	\$ 183,627	100%	\$ 157,069	100%

As a percentage of revenue, export revenue was 81% and 78% for the three and nine months ended September 30, 2006, respectively, and was 76% for both the three and nine months ended September 30, 2005. Export revenue as a percentage of overall revenue increased in the third quarter of 2006 compared to the same period in 2005 due to more favorable business conditions in Asia.

During the third quarter and first nine months of 2006 compared to the same periods in 2005, total units increased 16% and 15%, respectively, and average selling prices increased 2% for the quarter and nine months ended September 30,

2006, compared to the same periods in 2005. The increase in units sold during the third quarter and first nine months of 2006 compared to the same periods in 2005 was predominantly attributable to significantly higher unit volume of New and Mainstream products based on product categories as previously described. The increase in average selling price was primarily due to product mix, which was driven by the Company's increased FPGA revenue. Selling prices of our products generally decline over time. Higher selling prices of new products commonly offset some or all of this decline; however, product mix changes can also have a significant effect on average selling prices.

Our ability to achieve revenue growth is in large part dependent on the continued development, introduction and market acceptance of new products.

Gross Margin and Operating Expenses

Key elements of our Condensed Consolidated Statement of Operations, expressed as a percentage of revenue, were as follows:

	Three Months Ended (1)		Nine Months Ended (1)	
	September 30, 2006	September 30, 2005	September 30, 2006	September 30, 2005
Revenue	100.0%	100.0%	100.0%	100.0%
Gross margin	56.1%	56.2%	56.3%	56.4%
Research and development expenses	33.8%	43.4%	34.7%	46.7%
Selling, general and administrative expenses	22.8%	25.4%	22.3%	28.2%
Amortization of intangible assets	4.2%	6.6%	4.4%	6.9%
Loss from operations	(4.8)%	(19.2)%	(5.2)%	(25.4)%

(1) Our Statement of Operations includes the effect of stock-based compensation as previously quantified in Note 4 to these financial statements.

Our gross margin percentage remained relatively constant at 56.1% in the third quarter of 2006 and 56.3% for the first nine months of 2006, compared to 56.2% and 56.4% in the third quarter and first nine months of 2005, respectively. Gross margin changes primarily reflect the effects of varying product mix. The gross margin on new products is initially lower than average gross margins. We typically experience improved gross margins on new products as the result of wafer and assembly price reductions and improved wafer and assembly yields. Accordingly, our gross margins may vary as a result of the timing and volume of new product introductions and the varying product mix of new, mainstream and mature products.

Research and development expenses decreased to \$21.5 million (34% of revenue) and \$63.7 million (35% of revenue) in the third quarter and first nine months of 2006, respectively, compared to \$23.2 million (43% of revenue) and \$73.4 million (47% of revenue) in the third quarter and first nine months of 2005, respectively. The decrease in expense is primarily a result of the restructuring plan implemented during the fourth quarter of 2005 and the elimination of stock compensation expense related to acquisitions. Although we experienced a considerable decline in research and development expense during the three and nine months ended September 30, 2006 compared to the comparable periods in 2005, we believe that a continued commitment to research and development is essential in order to achieve and maintain product leadership and provide innovative new product offerings, and therefore we expect to continue to make significant future investments in research and development. As we transition to more advanced process technologies such as 90 nanometer and beyond, mask and engineering wafer costs are becoming increasingly more expensive and will therefore increasingly represent a greater proportion of total research and development expenses.

Selling, general and administrative expenses increased to \$14.5 million (23% of revenue) in the third quarter of 2006 and decreased to \$40.9 million (22% of revenue) in the first nine months of 2006, compared to \$13.6 million (25% of revenue) and \$44.3 million (28% of revenue) in the third quarter and first nine months of 2005, respectively. The increase in expense for the three months ended September 30, 2006 compared to the same period in 2005 is primarily attributable to an increase in commission and compensation related expenses of \$1.5 million, offset by a reduction of legal expenses of \$0.6 million. The decrease in expenses for the nine months ended September 30, 2006 compared to the same period in 2005 is related to a decrease in legal expenses of approximately \$5.5 million, primarily offset by an increase in commission and compensation related expenses of \$2.0 million. These legal expenses were

related to the shareholder class action suits, the then pending (now settled) derivative suit and related Audit Committee examination and Special Litigation Committee ("SLC") investigation (now concluded), and the Securities and Exchange Commission's informal inquiry.

Amortization of intangible assets declined to \$2.7 million and \$8.1 million in the third quarter and nine months of 2006, respectively, compared to \$3.5 million and \$10.9 million for the same periods in 2005. Amortization charges related to previously completed acquisitions (from 2002 and earlier) are expected to be substantially eliminated in 2008.

Other Income, net

Interest income was \$3.6 million and \$9.7 million in the third quarter and first nine months of 2006, respectively, compared to \$2.3 million and \$5.9 million for the same periods of 2005. The increase was a result of higher interest rates on marketable securities.

Other income, net, of \$4.2 million in the third quarter of 2006 included a \$0.8 million gain on the extinguishment of \$10.0 million of Convertible Notes for \$9.2 million in cash. Amortization of Convertible Notes issuance costs was \$0.1 million during the quarter. For the nine-month period ended September 30, 2006, other income, net, of \$12.5 million included a net gain on the sale of UMC common stock of \$1.3 million, and a gain of \$2.0 million on the extinguishment of \$20.0 million of Convertible Notes for \$17.8 million in cash, which required recognition of \$0.1 million of unamortized issuance costs. Amortization of Convertible Notes issuance costs was \$0.4 million for the first nine months of 2006.

Other income, net, of \$3.4 million in the third quarter of 2005 primarily included a \$1.4 million gain on the extinguishment of \$10.0 million of our Convertible Notes for \$8.5 million in cash, which required recognition of \$0.1 million of unamortized issuance costs. Amortization of Convertible Notes issuance costs was \$0.2 million in the third quarter of 2005. For the nine-month period ended September 30, 2005, other income, net, of \$14.3 million primarily included a net gain on the sale of UMC common stock of \$4.3 million and a gain of \$4.9 million on the extinguishment of \$35.5 million of Convertible Notes for \$30.2 million in cash, which required recognition of \$0.4 million of unamortized issuance costs. Amortization of Convertible Notes issuance costs was \$0.7 million for the first nine months of 2005.

We are paying foreign income taxes, which are reflected in the Condensed Consolidated Statement of Operations and are primarily related to the cost of operating our offshore research and development and sales subsidiaries. We expect to continue to pay foreign income taxes at current levels. We are not currently paying federal or state income taxes and do not expect to pay such taxes until the benefits of our tax net operating losses are fully utilized. We accrue interest expense on our tax contingency reserve.

New Accounting Pronouncements:

In March 2006, the Emerging Issues Task Force reached a consensus on Issue No. 06-03 "How Taxes Collected from Customers and Remitted to Government Authorities Should Be Presented in the Income Statement (That Is, Gross versus Net Presentation)" ("EITF No. 06-03"). The Company is required to adopt the provisions of EITF No. 06-03 beginning in fiscal year 2007. The Company does not expect the provisions of EITF No. 06-03 to have a material impact on the Company's consolidated financial position, results of operations or cash flows.

In June 2006, the Financial Accounting Standards Board ("FASB") issued Interpretation ("Interpretation") No. 48, "Accounting for Uncertainty in Income Taxes - an interpretation of FASB Statement No. 109." This Interpretation prescribes a recognition threshold and measurement attribute for the financial statement recognition and measurement of a tax position taken or expected to be taken in a tax return, and provides guidance on derecognition, classification, interest and penalties, accounting in interim periods, disclosure, and transition. This Interpretation is effective for fiscal years beginning after December 15, 2006. We are currently assessing the impact of this Interpretation on our consolidated financial statements.

In September 2006, the FASB issued SFAS No. 157, "Fair Value Measurements" ("SFAS No. 157") which defines fair value of certain assets and liabilities, establishes a framework for measuring fair value, and expands disclosures about fair value measurements. This statement does not require any new fair value measurements, but may change current practice for certain entities. This statement is effective for financial statements issued for fiscal years beginning after November 15, 2007, and interim periods within those years. We are currently assessing the impact of SFAS No. 157 on our consolidated financial statements.

In September 2006, the U.S. Securities and Exchange Commission (SEC) issued Staff Accounting Bulletin No. 108, "Considering the Effects of Prior Year Misstatements when Quantifying Misstatements in Current Year Financial Statements" ("SAB 108"). SAB 108 addresses the diversity in practice of quantifying financial statement misstatements resulting in the potential build up of improper amounts on the balance sheet. SAB 108 provides interpretive guidance on how the effects of the carryover or reversal of prior year misstatements should be considered in quantifying a current year misstatement. The SEC staff believes that registrants should quantify errors using both a balance sheet and an income statement approach and evaluate whether either approach results in quantifying a misstatement that, when all relevant quantitative and qualitative factors are considered, is material. SAB 108 is effective for companies with fiscal years ending after November 15, 2006. SAB 108 allows a one-time transitional cumulative effect adjustment to beginning retained earnings, in the first year of adoption, for errors that were not previously deemed material, but are material under the guidance in SAB 108. We are currently assessing the impact of SAB 108 on our consolidated financial statements.

Liquidity and Capital Resources:

Financial Condition (Sources and Uses of Cash):

Operating

Net cash provided by operating activities increased by approximately \$40.5 million in the nine months ended September 30, 2006 as compared to the corresponding period in fiscal 2005. The change is primarily due to recognition of net income of \$2.2 million for the nine months ended September 30, 2006 compared to a net loss of \$26.1 million for the nine months ended September 30, 2005. In addition, an advance payment to Fujitsu of \$25.0 million occurred during the nine months ended September 30, 2005 that did not occur in the nine months ended September 30, 2006. Cash from operating activities related to accounts receivable collections provided approximately \$7.5 million in the nine months ended September 30, 2006 as compared to the corresponding period

in fiscal 2005. Inventories increased by approximately \$8.0 million in the nine months ended September 30, 2006 compared to a decrease of \$6.4 million in the corresponding period in fiscal 2005 due primarily to increased inventories related to New products.

Investing

Net cash provided by investing activities decreased by \$49.9 million for the nine months ended September 30, 2006 as compared to the corresponding period in 2005. The change is due primarily to decreases in both the sale of short-term investments and the sale of equity securities (principally UMC common stock). Net proceeds from short-term investments totaled \$4.4 million for the nine months ended September 30, 2006 compared to \$34.1 million for the same period in 2005. Likewise, net proceeds from the sale of equity securities (principally UMC common stock) totaled \$10.2 million for the nine months ended September 30, 2006 compared to \$27.5 million for the same period in 2005. Further, capital expenditures have been greater in the first nine months of 2006, totaling \$10.9 million compared to \$8.0 million in the first nine months in 2005, which is primarily related to purchases of new test equipment.

Financing

Net cash used in financing activities decreased by \$16.2 million in the nine months ended September 30, 2006 as compared to the corresponding period in 2005. The decrease is due primarily to the use of \$17.8 million to extinguish our Convertible Notes in 2006 compared to \$30.2 million in 2005. Further, net proceeds from the issuance of common stock for 2006 increased \$4.1 million over the same period in 2005. These changes were offset by a decrease in net advances on the Yen line of credit to \$1.2 million for the first nine months of 2006 compared to \$1.6 million for the first nine months of 2005.

Liquidity:

As of September 30, 2006, our principal source of liquidity was \$268.1 million of cash and marketable securities, which represents a \$3.9 million increase compared to the balance at December 31, 2005. Our working capital at September 30, 2006 was \$302.6 million, compared to \$278.4 million at December 31, 2005, an increase of \$24.2 million. Most of this increase in liquidity and working capital for the nine months ended September 30, 2006 was derived from the increase in cash flows from operations and proceeds from the sale of UMC stock, which more than offset the early payment of \$17.8 million to extinguish \$20.0 million principal amount of our Convertible Notes (which are due July 1, 2010), and our capital expenditures, which we expect to be approximately \$13.0 million to \$15.0 million for the year ending December 31, 2006.

We believe that our existing liquid resources and expected cash generated from future operations, combined with wafer credits from foundries and our ability to borrow additional funds, will be adequate to meet our operating and capital requirements and obligations for the next 12 months. Included in such anticipated obligations are advance payments to Fujitsu of \$37.5 million in the fourth quarter of 2006 and \$37.5 million in the first quarter of 2007 (the first \$37.5 million based on the achieved milestone in the fourth quarter of 2006, and the final \$37.5 million based on a milestone expected to be achieved in the fourth quarter of 2006). These payments represent the final two installments to complete the unsecured advance payments of \$125.0 million to Fujitsu for prepaid wafers. The advance payments will be returned to us in the form of wafer credits or other repayment, subject to the right of either party to terminate the agreement upon the occurrence of certain events. At September 30, 2006, \$1.1 million had been returned to us in the form of wafer credits, and we expect an additional \$20.5 million in wafer credits during the next twelve months.

From 2003 through September 30, 2006, the Company has paid an aggregate of \$74.1 million to extinguish \$86.5 million principal amount of Convertible Notes, which are due July 1, 2010. From time to time the Company will assess its liquidity and capital resources, and overall economic and market conditions, and may repurchase additional Convertible Notes prior to their due date. While the Convertible Notes are due for payment in 2010, holders of the Convertible Notes have a right to require payment of the Convertible Notes on July 1, 2008. As of September 30, 2006, \$113.5 million principal amount of the Convertible Notes remain outstanding.

We may in the future seek new or additional sources of funding. In addition, in order to secure additional wafer supply, we may from time to time consider various financial arrangements including equity investments, advance purchase payments, loans, or similar arrangements with independent wafer manufacturers in exchange for committed wafer capacity. To the extent that we pursue any such additional financing arrangements, additional debt or equity financing may be required. There can be no assurance that such additional financing will be available when needed or, if available, will be on favorable terms. Any future equity financing will decrease existing stockholders' equity percentage ownership and may, depending on the price at which the equity is sold, result in dilution.

Contractual Obligations:

Our only significant operating leases, apart from those accrued for in the restructuring, are for our facilities in San Jose, California and Bethlehem, Pennsylvania. Our lease in San Jose expires in December of 2008. Annual rent is \$4.0 million and increases approximately 3% annually. Our lease in Bethlehem expires in August of 2009. Annual rent is \$0.8 million and increases approximately 3% annually. Leasehold improvements are amortized over the shorter of the non-cancelable lease term or the estimated useful life of the assets.

On September 10, 2004, we entered into an Advance Payment and Purchase Agreement (the "Fujitsu APP Agreement") with Fujitsu Limited ("Fujitsu"), pursuant to which we will advance \$125.0 million to Fujitsu. The initial two payments of \$25.0 million each were made in October 2004 and January 2005. The remaining payments will be made in two stages upon the achievement of certain milestones and will be recorded upon completion of the related milestone. The third milestone has been achieved, and as a result, we will pay \$37.5 million in the fourth quarter of 2006. We currently anticipate that the final milestone will be met in the fourth quarter of 2006 and we will pay the final \$37.5 million in the first quarter of 2007.

During the third quarter of 2006, we entered into an amendment ("Amendment") to the Fujitsu APP Agreement. Prior to the Amendment, our \$125.0 million advance was to be credited against the purchase price of 300mm wafers received from Fujitsu. The Amendment permits us to also credit the advance against the purchase price of 200mm wafers. The Fujitsu APP Agreement will continue until the full amount of the advance payment has been returned to us in the form of wafer credits or other repayment, subject to the right of either party to terminate the agreement upon the occurrence of certain events. Prior to the Amendment, we could request a refund of the unused amount of the advance payment if we have not used all of our wafer credits by December 31, 2007. Pursuant to the Amendment, we may request a refund of the unused amount of the advance payment if we have not used all of our wafer credits by December 31, 2008. The repayment obligation of Fujitsu is unsecured.

There have been no other material changes outside of the ordinary course of business in our contractual obligations and rights since the filing of our Annual Report on Form 10-K.

Off-Balance Sheet Arrangements:

As of September 30, 2006, we did not have any off-balance sheet arrangements, as defined in Item 303(a)(4)(ii) of SEC Regulation S-K.

ITEM 3. QUANTITATIVE AND QUALITATIVE DISCLOSURES ABOUT MARKET RISK

The fair market value of the Convertible Notes is subject to interest rate risk and market risk due to the convertible feature of the Convertible Notes, the fair market value of the Company's common stock, and other factors. Changes in the fair market value of the Convertible Notes does not impact our financial position, cash flows or results of operations. As of September 30, 2006 and December 31, 2005, the fair market value of the Convertible Notes was approximately \$107.0 million and \$114.0 million, respectively, based on market prices. While the Convertible Notes are due for payment in 2010, holders of the Convertible Notes have a right to require payment of the Convertible Notes on July 1, 2008.

Except as described in the preceding paragraph, there have been no other material changes to the quantitative and qualitative disclosures about market risk reported in our Annual Report on Form 10-K for the year ended December 31, 2005.

ITEM 4. CONTROLS AND PROCEDURES

Based on their evaluation as of the end of the period covered by this Quarterly Report on Form 10-Q, the Company's Chief Executive Officer and Chief Financial Officer have concluded that the Company's "disclosure controls and procedures" (as defined in Rules 13a-15(e) and 15d-15(e) under the Securities Exchange Act of 1934 (the "Exchange Act")) are effective.

In connection with the evaluation described above, we identified no change in our internal control over financial reporting that occurred during the three months ended September 30, 2006, that has materially affected, or is reasonably likely to materially affect, the Company's internal control over financial reporting.

PART II. OTHER INFORMATION

ITEM 1. LEGAL PROCEEDINGS

In September and October 2004, three putative class action complaints were filed in the United States District Court for the District of Oregon against Lattice Semiconductor Corporation, our Chief Executive Officer and President Stephen A. Skaggs, and our former Chief Executive Officer Cyrus Y. Tsui. These complaints were filed on behalf of a putative class of investors who purchased our stock between April 22, 2003 and April 19, 2004. They generally alleged violations of federal securities laws arising out of our previously announced restatement of financial results for the first, second, and third quarters of 2003. Consistent with the usual procedures for cases of this kind, these cases were amended and consolidated into a single action. In an amended and consolidated complaint filed January 27, 2005 our former President and our former Controller were added as defendants. The complaints generally sought an unspecified amount of damages, as well as attorney fees and costs. The Company has entered into an agreement with the plaintiffs to settle the consolidated action. The agreement, which has preliminary court approval, does not contain any admission of fault or wrongdoing on the part of the Company or any of the individual defendants in the litigation. The agreement is subject to, and conditioned upon, final court approval. The agreement provides that plaintiffs will receive an aggregate amount of \$3.5 million, inclusive of fees and expenses of counsel, in exchange for a release of the Company and the individual defendants from all claims asserted in the litigation. Subject to final approval by the Court, the Company's insurance carriers have agreed with the Company to pay the entire amount, on behalf of the Company, to settle the suit under the terms of the Company's director and officer liability insurance policy. The Company has recorded a liability in its financial statements for the proposed amount of the settlement. Additionally, because the insurance carriers have agreed to pay the entire \$3.5 million settlement amount, a receivable was also recorded for the same amount. Accordingly, there is no impact to the Condensed Consolidated Statement of Operations as the amounts of the settlement and the insurance recovery fully offset each other.

On June 14, 2005, we announced that our Audit Committee, in connection with its responsibilities for financial oversight, was conducting an internal examination. The examination concerned issues primarily associated with executive compensation and several items pertaining to our internal controls. On August 9, 2005, we announced that the Audit Committee had concluded the examination. We have furnished information regarding the matters examined by the Audit Committee to the Securities and Exchange Commission, which has been conducting an informal inquiry into our prior restatement of financial results. On September 30, 2005 the Securities and Exchange Commission issued a Cease and Desist Order concerning our former Controller, and referenced certain events in connection with our prior restatement of financial results. We are cooperating fully with the Securities and Exchange Commission and intend to continue to do so. In September 2006, we made a settlement offer to the Securities and Exchange Commission to resolve the informal inquiry. We offered to consent to the entry of a cease and desist order with respect to violations of Sections 13(a) and 13(b)(2)(A)-(B) of the Securities Exchange Act of 1934, as amended, and Rule 13a-13 promulgated thereunder. We will not be required to pay

any penalties in connection with the settlement. The proposed settlement is subject to final approval by the Securities and Exchange Commission. The Securities and Exchange Commission staff has agreed to recommend such approval. However, if we do not reach agreement with the Securities and Exchange Commission to settle its informal inquiry or if the Securities and Exchange Commission decides to pursue enforcement action against us, or any other governmental agency or regulatory body takes similar action, our management could be distracted, we could incur substantial costs and there could be a material adverse effect on our business.

We are exposed to certain asserted and unasserted potential claims. There can be no assurance that, with respect to potential claims made against us, we could resolve such claims under terms and conditions that would not have a material adverse effect on our business, our liquidity, our financial position or our operating results.

ITEM 1A. RISK FACTORS

The risk factors included herein include any material changes to and supersede the description of the risk factors associated with our business previously disclosed in Item 1A of our Annual Report on Form 10-K for the year ended December 31, 2005. The following risk factors and other information included in this Quarterly Report should be carefully considered. The risks and uncertainties described below are not the only ones we face. Additional risks and uncertainties not presently known to us or that we currently deem immaterial also may impair our business operations. If any of the following risks occur, our business, financial condition, operating results, and cash flows could be materially adversely affected.

We may be unsuccessful in defining, developing or selling the new FPGA products required to maintain or expand our business.

As a semiconductor company, we operate in a dynamic environment marked by rapid product obsolescence. The programmable logic market is characterized by rapid technology and product evolution and historically the market for FPGA products has grown faster than the market for PLD products. Currently we derive a greater proportion of our revenue from PLD products than FPGA products. Consequently, our future success depends on our ability to introduce new FPGA and associated software design tool products that meet evolving customer needs while achieving acceptable margins. We are presently shipping our next generation FPGA product families that are critical to our ability to grow our FPGA product revenue and expand our overall revenue. We also plan to continue upgrading our customer design tool products and increase our offerings of intellectual property cores. If we fail to introduce new products in a timely manner, or if these products or future new products fail to achieve market acceptance, our operating results would be harmed.

Fujitsu has agreed to manufacture our current and future FPGA products on its 130 nanometer and 90 nanometer CMOS process technologies, as well as on 130 nanometer and 90 nanometer technologies with embedded flash memory that we have jointly developed with Fujitsu. We are also developing 65 nanometer CMOS process technology with Fujitsu. The success of our next generation FPGA products is dependent on our ability to successfully partner with Fujitsu. If for any reason we are unsuccessful in our efforts to partner with Fujitsu in connection with these next generation FPGA products, our future revenue growth would be materially adversely affected.

The introduction of new silicon and software design tool products in a dynamic market environment presents significant business challenges. Product development commitments and expenditures must be made well in advance of product sales. The market reception of new products depends on accurate projections of long-term customer demand, which by their nature are uncertain.

Our future revenue growth is dependent on market acceptance of our new silicon and software design tool products and the continued market acceptance of our current products. The success of these products is dependent on a variety of specific technical factors including:

- successful product definition;
- timely and efficient completion of product design;
- timely and efficient implementation of wafer manufacturing and assembly processes;
- product performance;
- product cost; and
- the quality and reliability of the product.

If, due to these or other factors, our new silicon and software products do not achieve market acceptance, our operating results would be harmed.

The potential impact of customer design-in activity on future revenue is inherently uncertain and could impact our ability to manage production or our ability to forecast sales.

We face uncertainties relating to the potential impact of customer design-in activity because it is unknown whether any particular customer design-in will ultimately result in sales of significant volume. After a specific customer design-in is obtained, many factors can impact the timing and amount of sales that are ultimately realized from the specific customer design-in. Changes in the competitive position of our technology, our customer's product competitiveness, our customer's product strategy, the financial position of our customer, and many other factors can all impact the timing and amount of sales ultimately realized from any specific customer design-in. As a result, we may not be able to accurately manage the production levels of our new products or accurately forecast the future sales of such products, and, thus, our operating results could be harmed.

Our products may not be competitive if we are unsuccessful in migrating our manufacturing processes to more advanced technologies or alternative fabrication facilities.

To develop new products and maintain the competitiveness of existing products, we need to migrate to more advanced wafer manufacturing processes that use smaller device geometries. We also may need to use additional foundries. Because we depend upon foundries to provide their facilities and support for our process technology development, we may experience delays in the availability of advanced wafer manufacturing process technologies at

existing or new wafer fabrication facilities. As a result, volume production of our advanced process technologies at the fabrication facilities of Fujitsu, UMC or future foundries may not be achieved. This could harm our operating results.

Our supply of assembled and tested products could be interrupted or reduced, which may result in a shortage of products available for sale.

We do not assemble our finished products or perform all testing of our products. Currently, our finished products are assembled and tested by ASE in Malaysia, Amkor in the Philippines and South Korea, Fujitsu in Japan, AIT in Indonesia, and other independent contractors in Asia. If any of our current or future assembly or test contractors significantly interrupts or reduces our supply of assembled and tested devices, our operating results could be harmed.

In the past, we have experienced delays in obtaining assembled and tested products and in securing assembly and test capacity commitments from our suppliers. At present, we anticipate that our assembly and test capacity commitments are adequate. However, these existing commitments may not be sufficient for us to satisfy customer demand in future periods. Additionally, notwithstanding our assembly and test capacity commitments we may still have difficulty in obtaining deliveries of finished products consistent with the capacity commitments. We negotiate assembly and test prices and capacity commitments from our contractors on a periodic basis. If any of our assembly or test contractors were to reduce its capacity commitment or increase its prices, and we cannot find alternative sources, our operating results could be harmed.

Many other factors that could disrupt our supply of finished products are beyond our control. Since worldwide capacity for assembly and testing of semiconductor products is limited and inelastic, we could be harmed by significant industry-wide increases in overall demand or interruptions in supply. The assembly of complex packages requires a consistent supply of a variety of raw materials such as substrates, leadframes, and mold compound. The worldwide manufacturing capacity for these materials is also limited and inelastic. A significant industry-wide increase in demand, or interruptions in the supply of these materials to our assembly or test contractors, could harm our operating results. Additionally, a future disruption of any of our assembly or test contractors' operations as a result of a fire, earthquake, act of terrorism, political unrest, governmental uncertainty, war, or other natural disaster or catastrophic event could disrupt our supply of assembled and tested devices and could harm our operating results.

In addition, our quarterly revenue levels may be affected to a significant extent by our ability to match inventory and current production mix with the product mix required to fulfill orders. The large number of individual parts we sell and the large number of customers for our products, combined with limitations on our and our customer's ability to forecast orders accurately and our relatively lengthy manufacturing cycles, may make it difficult to achieve a match of inventory on hand, production units, and shippable orders sufficient to realize quarterly or annual revenue projections.

Our wafer supply could be interrupted or reduced, which may result in a shortage of products available for sale.

We do not manufacture finished silicon wafers and many of our products, including all of our newest FPGA products, are manufactured by a sole source. Currently, our silicon wafers are manufactured by Fujitsu in Japan, Seiko Epson in Japan, UMC in Taiwan and Chartered Semiconductor in Singapore. If any of our current or future foundry partners significantly interrupts or reduces our wafer supply, our operating results could be harmed.

In the past, we have experienced delays in obtaining wafers and in securing supply commitments from our foundries. At present, we anticipate that our supply commitments are adequate. However, these existing supply commitments may not be sufficient for us to satisfy customer demand in future periods. Additionally, notwithstanding our supply commitments, we may still have difficulty in obtaining wafer deliveries consistent with the supply commitments. We negotiate wafer prices and supply commitments from our suppliers on at least an annual basis. If any of our foundry partners were to reduce its supply commitment or increase its wafer prices, and we cannot find alternative sources of wafer supply, our operating results could be harmed.

Many other factors that could disrupt our wafer supply are beyond our control. Since worldwide manufacturing capacity for silicon wafers is limited and inelastic, we could be harmed by significant industry-wide increases in overall wafer demand or interruptions in wafer supply. Additionally, a future disruption of any of our foundry partners' foundry operations as a result of a fire, earthquake, act of terrorism, political unrest, governmental uncertainty, war, or other natural disaster or catastrophic event could disrupt our wafer supply and could harm our operating results.

If our foundry partners experience quality or yield problems, we may face a shortage of products available for sale.

We depend on our foundries to deliver reliable silicon wafers with acceptable yields in a timely manner. As is common in our industry, we have experienced wafer yield problems and delivery delays. If our foundries are unable for a prolonged period to produce silicon wafers that meet our specifications, with acceptable yields, our operating results could be harmed.

The reliable manufacture of high performance programmable logic devices is a complicated and technically demanding process requiring:

- a high degree of technical skill;
- state-of-the-art equipment;
- the availability of certain basic materials and supplies, such as chemicals, gases, polysilicon, silicon wafers and ultra-pure metals;
- the absence of defects in production wafers;
- the elimination of minute impurities and errors in each step of the fabrication process; and
- effective cooperation between the wafer supplier and us.

As a result, our foundries may experience difficulties in achieving acceptable quality and yield levels when manufacturing our silicon wafers.

If our assembly and test supply contractors experience quality or yield problems, we may face a shortage of products available for sale.

We rely on contractors to assemble and test our devices with acceptable quality and yield levels. As is common in our industry, we have experienced quality and yield problems in the past. If we experience prolonged quality or yield problems in the future, our operating results could be harmed.

The majority of our revenue is derived from semiconductor devices assembled in advanced packages. The assembly of advanced packages is a complex process requiring:

- a high degree of technical skill;
- state-of-the-art equipment;
- the absence of defects in assembly and packaging manufacturing;
- the elimination of raw material impurities and errors in each step of the process; and
- effective cooperation between the assembly contractor and us.

As a result, our contractors may experience difficulties in achieving acceptable quality and yield levels when assembling and testing our semiconductor devices.

Product quality problems could lead to reduced revenue, gross margins, and net income.

We generally warrant our products for varying lengths of time against non-conformance to our specifications and certain other defects. Because our products, including hardware and software, are highly complex and incorporate leading-edge technology, our quality assurance programs may not detect all defects, whether manufacturing defects in individual products or systematic defects that could affect numerous shipments. On occasion we have elected to or been required to repair or replace certain components and software or refund the purchase price or license fee paid by our customers due to product defects. If there are material increases in warranty claims or the costs to resolve warranty claims compared with our historical experience, our revenue, gross margins, and net income may be adversely affected. For example, an inability to cure a product defect in a timely manner could result in product reengineering expenses, increased inventory costs, or damage to our reputation, any of which could materially impact our revenue, gross margins, and net income.

The cyclical nature of the semiconductor industry may limit our ability to maintain revenue levels and operating results during industry downturns.

The semiconductor industry is highly cyclical, to a greater extent than other less technology-driven industries. Our financial performance has periodically been negatively affected by downturns in the semiconductor industry. Factors that contribute to these industry downturns include:

- the cyclical nature of the demand for the products of semiconductor customers;
- general reductions in inventory levels by customers;
- excess production capacity;
- general decline in end-user demand; and
- accelerated declines in average selling prices.

Historically, the semiconductor industry has experienced periodic downturns of varying degrees of severity and duration. Typically, after such downturns, semiconductor industry conditions improve, although such improvement may not be significant or sustainable. Increased demand for semiconductor industry products may not proportionately increase demand for programmable logic products in general, or our products in particular. Even if demand for our products increases, average selling prices for our products may not increase, and could decline. Whenever adverse semiconductor industry conditions or other similar conditions exist, there is likely to be an adverse effect on our operating results.

Further, our ability to predict end-user demand is limited. Typically, the majority of our revenue comes from “turns orders,” which are orders placed and filled within the same quarter. By definition, turns orders are not captured in a backlog measurement at the beginning of a quarter. Accordingly, we cannot use backlog as a reliable measure of predicting revenue.

A downturn in the communications equipment end market or computing end market could cause a reduction in demand for our products and limit our ability to maintain revenue levels and operating results.

The majority of our revenue is derived from customers in the communications equipment and computing end markets. Any deterioration in these end markets or any reduction in technology capital spending could lead to a reduction in demand for our products. For example, in the past, a general weakening in demand for programmable logic products from customers in the communications end market has adversely affected our revenue. Whenever adverse economic or end market conditions exist, there is likely to be an adverse effect on our operating results.

Conditions in Asia may disrupt our existing supply arrangements and result in a shortage of finished products available for sale.

All of our major silicon wafer suppliers operate fabrication facilities located in Asia. Additionally, our finished silicon wafers are assembled and tested by independent contractors located in Malaysia, South Korea, the Philippines, Indonesia and Japan. Economic, financial, social and political conditions in Asia have historically been volatile. Financial difficulties, the effects of currency fluctuation, governmental actions or restrictions, prolonged work stoppages, political unrest, war, natural disaster, disease or any other difficulties experienced by our suppliers may disrupt our supply and could harm our operating results.

Export sales account for the majority of our revenue and may decline in the future due to economic and governmental uncertainties.

We derive a majority of our revenue from export sales. Accordingly, if we experience a decline in export sales, our operating results could be harmed. Our export sales are subject to numerous risks, including:

- changes in local economic conditions;
- exchange rate volatility;
- governmental controls and trade restrictions;
- export license requirements and restrictions on the export of technology;
- political instability, war or terrorism;
- changes in tax rates, tariffs or freight rates;
- interruptions in air transportation; and
- difficulties in staffing and managing foreign sales offices.

Our future quarterly operating results may fluctuate and therefore may fail to meet expectations.

Our quarterly operating results have fluctuated in the past and may continue to fluctuate. Consequently, our operating results may fail to meet the expectations of analysts and investors. As a result of industry conditions and the following specific factors, our quarterly operating results are more likely to fluctuate and are more difficult to predict than a typical non-technology company of our size and maturity:

- general economic conditions in the countries where we sell our products;
- conditions within the end markets into which we sell our products;
- the cyclical nature of demand for our customers' products;
- excessive inventory accumulation by our end customers;
- the timing of our and our competitors' new product introductions;
- product obsolescence;
- the scheduling, rescheduling and cancellation of large orders by our customers;
- the willingness and ability of our customers and distributors to make payment to us in a timely manner;
- our ability to develop new process technologies and achieve volume production at the fabrication facilities of Fujitsu, UMC, Chartered Semiconductor or at other foundries;
- changes in manufacturing yields including delays in achieving target yields on new products;
- adverse movements in exchange rates, interest rates or tax rates; and
- the availability of adequate supply commitments from our wafer foundries and assembly and test subcontractors.

Our quarterly results may also fluctuate as a result of the legal expenses accrued in any given period in connection with the Securities and Exchange Commission's informal inquiry. As a result of these factors, our past financial results are not necessarily a good predictor of our future results.

We may have failed to adequately insure against certain risks, and, as a result, our financial condition and results may be adversely affected.

We carry insurance customary for companies in our industry, including, but not limited to, liability, property and casualty, worker's compensation and business interruption insurance. We also carry basic medical insurance, subject to a true

insurance stop loss for catastrophic illness. In addition, we have insurance contracts that provide director and officer liability coverage for our directors and officers. Other than the specific areas mentioned above, we are self-insured with respect to most other risks and exposures, and the insurance we carry in many cases is subject to a significant policy deductible or other limitation before coverage applies. Based on management's assessment and judgment, we have determined that it is more cost effective to self-insure against certain risks than to incur the insurance premium costs. The risks and exposures for which we self-insure include, but are not limited to, natural disasters, product defects, political risk, theft, patent infringement and some employment practice

matters. Should there be a catastrophic loss due to an uninsured event such as an earthquake or a loss due to adverse occurrences in any area in which we are self-insured, our financial condition, results of operations and liquidity may be adversely affected.

We face risks related to litigation and an informal inquiry by the Securities and Exchange Commission.

On June 14, 2005, we announced that our Audit Committee, in connection with its responsibilities for financial oversight, was conducting an internal examination. The examination concerned issues primarily associated with executive compensation and several items pertaining to our internal controls. On August 9, 2005, we announced that the Audit Committee had concluded the examination. We have furnished information regarding the matters examined by the Audit Committee to the Securities and Exchange Commission, which has been conducting an informal inquiry into our prior restatement of financial results. On September 30, 2005 the Securities and Exchange Commission issued a Cease and Desist Order concerning our former Controller, and referenced certain events in connection with our prior restatement of financial results. We are cooperating fully with the Securities and Exchange Commission and intend to continue to do so. In September 2006, we made a settlement offer to the Securities and Exchange Commission to resolve the informal inquiry. We offered to consent to the entry of a cease and desist order with respect to violations of Sections 13(a) and 13(b)(2)(A)-(B) of the Securities Exchange Act of 1934, as amended, and Rule 13a-13 promulgated thereunder. We will not be required to pay any penalties in connection with the settlement. The proposed settlement is subject to final approval by the Securities and Exchange Commission. The Securities and Exchange Commission staff has agreed to recommend such approval. However, if we do not reach agreement with the Securities and Exchange Commission to settle its informal inquiry or if the Securities and Exchange Commission decides to pursue enforcement action against us, or any other governmental agency or regulatory body takes similar action, our management could be distracted, we could incur substantial costs and there could be a material adverse effect on our business.

We are exposed to certain asserted and unasserted potential claims. There can be no assurance that, with respect to potential claims made against us, we could resolve such claims under terms and conditions that would not have a material adverse effect on our business, our liquidity or our financial results. We have been and may in the future be subject to various other legal proceedings, including, as discussed in greater detail hereafter, claims that involve possible infringement of patent and other intellectual property rights of third parties. It is inherently difficult to assess the outcome of litigation matters, and there can be no assurance that we will prevail in any litigation. Any such litigation could result in a substantial diversion of our efforts and the use of substantial management and financial resources, which by itself could have a material adverse effect on our financial condition and operating results. Further, an adverse determination in any such litigation could result in a material adverse impact on our financial position and the results of operations for the period in which the effect of an unfavorable final outcome becomes probable and reasonably estimable.

If we are unable to effectively and efficiently improve our internal controls in response to changing business, accounting and regulatory factors there could be a material adverse effect on our operations or financial results.

No assurance can be given that we will be able to successfully maintain, change and enhance as appropriate, our internal controls and procedures, or that any changes or enhancements to our controls and procedures will have the desired effect. In addition, we may be required to hire additional employees, and may experience higher than anticipated capital expenditures and operating expenses, during the implementation of any changes and enhancements and thereafter. Furthermore, future assessments of our internal controls and procedures may reveal material weaknesses. If we are unable to maintain, and effectively and efficiently change and enhance as appropriate, our internal controls and procedures, or if we discover material weaknesses, there could be a material adverse effect on our operations or financial results.

Changes in accounting for equity compensation will adversely affect our consolidated statement of operations and could adversely affect our ability to attract and retain employees.

We have historically used stock options as a key component of employee compensation in order to align employees' interests with the interests of our stockholders, encourage employee retention, and provide competitive compensation packages. The Financial Accounting Standards Board has adopted changes to generally accepted accounting principles that require us and other companies to record a charge to earnings for employee stock option grants and other equity incentives beginning in the quarter ended March 31, 2006. For the three and nine month periods ended September 30, 2006 we recorded \$1.1 million and \$2.5 million of stock-based compensation expense, respectively. To the extent that these or other new

regulations make it more difficult or expensive to grant options to employees, we will incur increased compensation costs. We may also change our equity compensation strategy, and this could make it difficult to attract, retain and motivate employees. Any of these results could materially and adversely affect our business.

Our stock price may continue to experience large fluctuations.

Historically, the price of our common stock has at times experienced rapid and severe price fluctuations that have left investors little time to react. The price of our common stock may continue to fluctuate greatly in the future due to a variety of company specific factors, including:

- quarter-to-quarter variations in our operating results;
- shortfalls in revenue or earnings from levels expected by investors;
- announcements of technological innovations or new products by other companies; and
- any developments in the Securities and Exchange Commission's informal inquiry.

At September 30, 2006, our book value per share was \$4.43 compared to our stock price, which has ranged from a low of \$4.20 per share to a high of \$7.55 per share for the nine months ended September 30, 2006. Presently, our stock price is trading above our consolidated book value. Should our stock price drop below book value for a sustained period, it may become necessary to record an impairment charge to goodwill, which would negatively impact our results of operations.

We may not be able to successfully compete in the highly competitive semiconductor industry.

The semiconductor industry is intensely competitive and many of our direct and indirect competitors have substantially greater financial, technological, manufacturing, marketing and sales resources. If we are unable to compete successfully in this environment, our future results will be adversely affected.

The current level of competition in the programmable logic market is high and may increase in the future. We currently compete directly with companies that have licensed our technology or have developed similar products. We also compete indirectly with numerous semiconductor companies that offer products based on alternative technical solutions. These direct and indirect competitors are established multinational semiconductor companies as well as emerging companies.

We may fail to retain or attract the specialized technical and management personnel required to successfully operate our business.

To a greater degree than most non-technology companies or larger technology companies, our future success depends on our ability to attract and retain highly qualified technical and management personnel. As a mid-sized company, we are particularly dependent on a relatively small group of key employees. Competition for skilled technical and management employees is intense within our industry. As a result, we may not be able to retain our existing key technical and management personnel. In addition, we may not be able to attract additional qualified employees in the future. If we are unable to retain existing key employees or are unable to hire new qualified employees, our operating results could be adversely affected.

If we are unable to adequately protect our intellectual property rights, our financial results and competitive position may suffer.

Our success depends in part on our proprietary technology. However, we may fail to adequately protect this technology. As a result, we may lose our competitive position or face significant expense to protect or enforce our intellectual property rights.

We intend to continue to protect our proprietary technology through patents, copyrights and trade secrets. Despite this intention, we may not be successful in achieving adequate protection. Claims allowed on any of our patents may not be sufficiently broad to protect our technology. Patents issued to us also may be challenged, invalidated or circumvented. Finally, our competitors may develop similar technology independently.

Companies in the semiconductor industry vigorously pursue their intellectual property rights. If we become involved in protracted intellectual property disputes or litigation we may be forced to use substantial financial and management resources, which could have an adverse effect on our operating results.

Our industry is characterized by frequent claims regarding patents and other intellectual property rights of others. We have been, and from time to time expect to be, notified of claims that we are infringing the intellectual property rights of

others. If any third party makes a valid claim against us, we could face significant liability and could be required to make material changes to our products and processes. In response to any claims of infringement, we may seek licenses under patents that we are alleged to be infringing. However, we may not be able to obtain a license on favorable terms, or at all, without our operating results being adversely affected.

ITEM 6. EXHIBITS

<u>Exhibits</u>	<u>Description</u>
10.53	Addendum dated March 22, 2006 to the Advance Payment and Purchase Agreement dated September 10, 2004 between Lattice Semiconductor Corporation and Fujitsu Limited.
10.54	Addendum dated effective October 1, 2006 to the Advance Payment and Purchase Agreement dated September 10, 2004 between Lattice Semiconductor Corporation and Fujitsu Limited. (1)
31.1	Certification of Chief Executive Officer pursuant to Rule 13a-14(a) of the Securities Exchange Act of 1934, as amended.
31.2	Certification of Chief Financial Officer pursuant to Rule 13a-14(a) of the Securities Exchange Act of 1934, as amended.
32.1	Certification of Chief Executive Officer Pursuant to 18 U.S.C. Section 1350, as adopted pursuant to Section 906 of the Sarbanes-Oxley Act of 2002.
32.2	Certification of Chief Financial Officer Pursuant to 18 U.S.C. Section 1350, as adopted pursuant to Section 906 of the Sarbanes-Oxley Act of 2002.

(1) Pursuant to Rule 246b-2 under the Securities Exchange Act of 1934, the registrant has applied contemporaneously for confidential treatment for portions of this exhibit, which portions have been deleted and filed separately with the Securities and Exchange Commission.

SIGNATURES

Pursuant to the requirements of the Securities Exchange Act of 1934, the Registrant has duly caused this report to be signed on its behalf by the undersigned thereunto duly authorized.

LATTICE SEMICONDUCTOR CORPORATION (Registrant)

/s/ JAN JOHANNESSEN

Jan Johannessen

*Senior Vice President and Chief Financial Officer
(Duly Authorized Officer and Principal Financial
and Accounting Officer)*

Date: November 7, 2006

**ADDENDUM TO
ADVANCE PAYMENT AND PURCHASE AGREEMENT**

THIS ADDENDUM TO ADVANCE PAYMENT AND PURCHASE AGREEMENT (this "Addendum") is entered into as of this 22nd day of March, 2006, by and between Lattice Semiconductor Corporation, having an office at 5555 NE Moore Court, Hillsboro, OR 97124 ("Lattice") and Fujitsu Limited, having an office at Akiruno Technology Center, 50 Fuchigami, Akiruno, Tokyo 197-0833, Japan ("Fujitsu") and Fujitsu Microelectronics America, Inc., having an office at 1250 East Arques Avenue, M/S333 Sunnyvale, CA 94088-3470, USA ("FMA").

WHEREAS, Lattice and Fujitsu have entered into an ADVANCE PAYMENT AND PURCHASE AGREEMENT dated September 10, 2004 ("APPA") in which Lattice agreed to provide an advance payment of money to Fujitsu to be used for the establishment of a new semiconductor wafer fabrication facility, including but not limited to the construction of the facilities and installation of fabrication equipments, and Fujitsu agreed to provide to Lattice 300mm Wafers using certain proprietary technologies;

WHEREAS, Lattice, Fujitsu and FMA have entered into or will enter into product supply agreements pursuant to which Lattice will buy from Fujitsu through FMA, and Fujitsu through FMA will supply to Lattice, the 300mm Wafers fabricated by Fujitsu; and

WHEREAS, Lattice, Fujitsu and FMA wish to confirm that sales and purchases of the 300mm Wafers under product supply agreements by and between Lattice, Fujitsu and FMA are also subject to the Advance Payment terms of the APPA.

NOW, THEREFORE, in consideration of the mutual promises herein, Lattice, Fujitsu and FMA hereby agree as follows:

1. Revision to Definition

Lattice and Fujitsu hereby amend the APPA as follow:

- (i) Section 1.17 of APPA shall be deleted and replaced in its entirety with the following;

"Product Supply Agreement(s)" will mean the agreement(s) by and among Lattice, Fujitsu and FMA for the Process Technology pursuant to which Fujitsu agrees to sell, and Lattice agrees to purchase, all through FMA, the 300mm Wafers for Products

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manufactured using such Process Technology

2. Application of Credit for Sales by FMA to Lattice

Lattice, FMA and Fujitsu acknowledge and agree that payment for all sales by FMA to Lattice of 300mm Wafers delivered to and accepted by Lattice under the Product Supply Agreement(s) during the term of the APPA shall be made by means of an offset against the remaining balance of the Credited Amount pursuant to the terms of the APPA to the greatest extent possible in the same manner and subject to the same terms of agreement of the APPA as originally provided for sales of 300mm Wafers by Fujitsu to Lattice. As part of this arrangement it is understood that FMA intends to assign to Fujitsu its accounts receivable arising from sales to Lattice of 300mm Wafers. The consideration for such assignment will be separately agreed between Fujitsu and FMA. The parties acknowledge and agree that they will amend relevant provisions in the existing Product Supply Agreements or will agree on such provisions in the Product Supply Agreement to be executed by the parties as necessary in order to provide for the offset against the Credited Amount as set forth above.

3. Effective Date

This Addendum shall become effective on 22nd March, 2006 and be in full force until the date on which the Credited Amount is fully offset and reduced to zero, or the APPA is terminated, whichever is earlier.

4. References

Words not expressly defined in this Addendum shall be construed in accordance with the definition in APPA.

5. No Other Changes

Except as expressly stated above, this Addendum does not otherwise amend APPA, which shall remain in full force and effect as amended hereby.

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IN WITNESS WHEREOF, the parties have signed this Addendum as of the date first above written.

**LATTICE SEMICONDUCTOR
CORPORATION**

FUJITSU LIMITED

By: /s/ Stephen Skaggs
Name: Stephen Skaggs

By: /s/ Tatsuya Yamazaki
Name: Tatsuya Yamazaki

Title: CEO
Date: 23 March 2006

Title: General Manager
Date: 27 March 2006

FUJITSU MICROELECTRONICS AMERICA, INC.

By: /s/ Kazuyuki Kawauchi
Name: Kazuyuki Kawauchi
Title: President and CEO
Date: 24 March 2006

Portions of this Agreement marked “*” have been redacted pursuant to a request for confidential treatment submitted to the Office of the Secretary of the Securities and Exchange Commission (the “SEC”). An unredacted version of this agreement has been filed separately with the SEC.

ADDENDUM #2 TO

ADVANCE PAYMENT AND PURCHASE AGREEMENT

THIS ADDENDUM #2 TO ADVANCE PAYMENT AND PURCHASE AGREEMENT (this “2nd Addendum”) is entered into with an effective date of October 1, 2006, by and between Lattice Semiconductor Corporation, having an office at 5555 NE Moore Court, Hillsboro, OR 97124 (“Lattice”) and Fujitsu Limited, having an office at Akiruno Technology Center, 50 Fuchigami, Akiruno, Tokyo 197-0833, Japan (“Fujitsu”) and Fujitsu Microelectronics America, Inc., having an office at 1250 East Arques Avenue, M/S333 Sunnyvale, CA 94088-3470, USA (“FMA”).

WHEREAS, Lattice and Fujitsu have entered into an ADVANCE PAYMENT AND PURCHASE AGREEMENT dated September 10, 2004 (“APPA”) and subsequently amended in an addendum dated March 22, 2006 (“1st Addendum”) in which Lattice agreed to provide an advance payment of money to Fujitsu to be used for the establishment of a new semiconductor wafer fabrication facility, including but not limited to the construction of the facilities and installation of fabrication equipments, and Fujitsu agreed to provide to Lattice 300mm Wafers using certain proprietary technologies;

WHEREAS, Lattice, Fujitsu and FMA have entered into or will enter into product supply agreements pursuant to which Lattice will buy from Fujitsu through FMA, and Fujitsu through FMA will supply to Lattice, the 300mm Wafers and 200mm Wafers (as defined in Section 1 below) fabricated by Fujitsu; and

WHEREAS, Lattice, Fujitsu and FMA wish to modify the APPA to extend by one year the earliest date on which Lattice may request repayment of advances under the APPA that have not at such time been applied toward wafer purchases, and also to make sales and purchases of 200mm Wafers (as defined in Section 1 below) subject to the APPA.

NOW, THEREFORE, in consideration of the mutual promises herein, Lattice Fujitsu and FMA hereby agree as follows:

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1. Addition to Definitions. The following terms are inserted as Sections 1.23 through 1.27 of the APPA:

1.23 “200mm Wafers” will mean Akiruno 200mm Wafers and Mie 200mm Wafers.

1.24 “Akiruno 200mm Wafers” will mean the 200mm semiconductor wafers fabricated by Fujitsu at Akiruno Facility for delivery and sale to Lattice under this Agreement and the 200mm Product Supply Agreements.

1.25 “Mie 200mm Wafers” will mean the 200mm semiconductor wafers fabricated by Fujitsu at the Fujitsu 200mm semiconductor wafer fabrication facility located in Mie, Japan for delivery and sale to Lattice under this Agreement and the 200mm Product Supply Agreements.

1.26 “200mm Price” will mean the price at which Fujitsu will sell and Lattice will purchase the 200mm Wafers and reticles therefor, pursuant to a 200mm Product Supply Agreement.

1.27 “200mm Product Supply Agreement(s)” will mean the agreement(s) by and among Lattice, Fujitsu and FMA pursuant to which Fujitsu agrees to sell and Lattice agrees to purchase, all through FMA, the 200mm Wafers for Lattice’s products.”

2. Change to Repayment Date. Section 8 of the APPA is deleted and replaced in its entirety with the following new section 8:

“If, for any reason, there remains a portion of the Credited Amount that has not been used to purchase 200mm Wafers or 300mm Wafers by December 31, 2008 (or such later date as may be mutually agreed), then Lattice will have the option, in its sole discretion and subject to the dispute resolution process provided in Section 13.2, to elect to receive repayment in cash of all or part of such then remaining outstanding portion of the Credited Amount. Fujitsu will repay any such amount within ninety (90) days of Lattice’s written request therefor, together with simple interest at the rate of one and seventy-five one hundredths percent (1.75%) per annum from the date of such written request.”

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3. Change to Term.

3.1 Section 12.1 of the APPA is deleted and replaced in its entirety with the following new section 12.1:

“The term of this Agreement will extend from the date first written above until all of the Credited Amount has been credited against 200mm Wafers and/or 300mm Wafers purchased hereunder or repaid to Lattice, unless terminated earlier pursuant to Section 12.2 or 12.3. After the expiration of this Agreement, any Product Supply Agreement in effect between the parties at such time will govern the relationship between the parties with respect to the manufacture, shipment, sale, and purchase of the 300mm Wafers provided under this Agreement, and any

200mm Product Supply Agreement in effect between the parties at such time will govern the relationship between the parties with respect to the manufacture, shipment, sale, and purchase of the 200mm Wafers provided under this Agreement.”

3.2 Section 12.4.4 of the APPA is deleted and replaced in its entirety with the following new section 12.4.4:

“If Fujitsu terminates this Agreement pursuant to Section 12.2 or 12.3.1, the parties will complete the purchase and sale of 300mm Wafers ordered in accordance with the applicable Product Supply Agreement and 200mm Wafers ordered in accordance with the applicable 200mm Product Supply Agreement, and offset the relevant Credited Amount. Fujitsu will repay the then remaining Credited Amount with no interest by the later of (i) December 31, 2008, or (ii) six (6) months from such termination.”

4. Credit of Advance Payment. Section 5.2 of the APPA is deleted and replaced in its entirety with the following new Section 5.2:

“The One Hundred Twenty-Five Million U.S. Dollars (U.S.\$125,000,000) Advance Payment (once paid, referred to as the “Credited Amount”) will be repaid by an offsetting adjustment in the amount of one hundred percent (100%) of the Price, as described in Section 7, for 300mm Wafers purchased under a Product Supply Agreement and one hundred percent (100%) of the 200mm Price for 200mm Wafers purchased under a 200mm Product Supply Agreement. The then current 300mm Price and/or 200mm Price, as applicable, for all purchases by Lattice of 300mm Wafers and 200mm Wafers will be offset cumulatively against the Credited Amount until the aggregate dollar value of the 300mm Price for all 300mm Wafers

purchased by Lattice, plus the aggregate dollar value of the 200mm Price for all 200mm Wafers purchased by Lattice, equals the amount of the Credited Amount.”

5. Free 200mm Wafers. Section 5.3 of the APPA shall be renumbered as 5.3.1 and the following shall be inserted as Section 5.3.2:

“As a consideration for Lattice’s payment of the Advance Payment, Fujitsu will provide Lattice with * free Akiruno 200mm Wafers and/or Mie 200mm Wafers, respectively, whenever the respective aggregated amount of the Akiruno 200mm Wafers or Mie 200mm Wafers shipped by Fujitsu reaches every * wafers while the Credited Amount exists, provided that, all the 200mm Wafers for which free 200mm Wafers will be provided are paid for by offsetting the 200mm Price thereof against the Credited Amount. The Credited Amount will not be reduced by the 200mm Price of such free Akiruno 200mm Wafers or Mie 200mm Wafers. Lattice will not receive free Akiruno 200mm Wafers or Mie 200mm Wafers with respect to any 200mm Wafers purchased by Lattice other than through reduction of the Credited Amount.”

6. The provisions of Sections 5.4, 5.5, and 7.3 of the APPA and Section 2 of the 1st Addendum will be applicable not only to 300mm Wafers, but also to 200mm Wafers delivered to and accepted by Lattice under the applicable 200mm Product Supply Agreement and paid for by means of an offset against the remaining balance of the Credited Amount.

IN WITNESS WHEREOF, the parties have signed this 2nd Addendum as of the date first above written.

LATTICE SEMICONDUCTOR CORPORATION

FUJITSU LIMITED

By: /s/ Tom Kingzett
Name: Tom Kingzett
Title: Corp. VP, Foundry Relations
Date: 28 September 2006

By: /s/ Tatsuya Yamazaki
Name: Tatsuya Yamazaki
Title: General Manager, Foundry and ASIC Marketing Division
Date: 22 September 2006

FUJITSU MICROELECTRONICS AMERICA, INC.

By: /s/ Keith Horn
Name: Keith Horn
Title: COO
Date: 27 September 2006

* denotes redaction pursuant to a request for confidential treatment.



CERTIFICATION

I, Stephen A. Skaggs, certify that:

1. I have reviewed this quarterly report on Form 10-Q of Lattice Semiconductor Corporation;
2. Based on my knowledge, this report does not contain any untrue statement of a material fact or omit to state a material fact necessary to make the statements made, in light of the circumstances under which such statements were made, not misleading with respect to the period covered by this report;
3. Based on my knowledge, the financial statements, and other financial information included in this report, fairly present in all material respects the financial condition, results of operations and cash flows of the registrant as of, and for, the periods presented in this report;
4. The registrant's other certifying officer(s) and I are responsible for establishing and maintaining disclosure controls and procedures (as defined in Exchange Act Rules 13a-15(e) and 15d-15(e)) and internal control over financial reporting (as defined in Exchange Act Rules 13a-15(f) and 15d-15(f)) for the registrant and have:
 - (a) Designed such disclosure controls and procedures, or caused such disclosure controls and procedures to be designed under our supervision, to ensure that material information relating to the registrant, including its consolidated subsidiaries, is made known to us by others within those entities, particularly during the period in which this report is being prepared;
 - (b) Designed such internal control over financial reporting, or caused such internal control over financial reporting to be designed under our supervision, to provide reasonable assurance regarding the reliability of financial reporting and the preparation of financial statements for external purposes in accordance with generally accepted accounting principles;
 - (c) Evaluated the effectiveness of the registrant's disclosure controls and procedures and presented in this report our conclusions about the effectiveness of the disclosure controls and procedures, as of the end of the period covered by this report based on such evaluation; and
 - (d) Disclosed in this report any change in the registrant's internal control over financial reporting that occurred during the registrant's most recent fiscal quarter (the registrant's fourth fiscal quarter in the case of an annual report) that has materially affected, or is reasonably likely to materially affect, the registrant's internal control over financial reporting.
5. The registrant's other certifying officer(s) and I have disclosed, based on our most recent evaluation of internal control over financial reporting, to the registrant's auditors and the audit committee of the registrant's board of directors (or persons performing the equivalent functions):
 - (a) All significant deficiencies and material weaknesses in the design or operation of internal control over financial reporting which are reasonably likely to adversely affect the registrant's ability to record, process, summarize and report financial information; and
 - (b) Any fraud, whether or not material, that involves management or other employees who have a significant role in the registrant's internal control over financial reporting.

Date: November 7, 2006

/s/ STEPHEN A. SKAGGS

Stephen A. Skaggs

President and Chief Executive Officer

CERTIFICATION

I, Jan Johannessen, certify that:

1. I have reviewed this quarterly report on Form 10-Q of Lattice Semiconductor Corporation;
2. Based on my knowledge, this report does not contain any untrue statement of a material fact or omit to state a material fact necessary to make the statements made, in light of the circumstances under which such statements were made, not misleading with respect to the period covered by this report;
3. Based on my knowledge, the financial statements, and other financial information included in this report, fairly present in all material respects the financial condition, results of operations and cash flows of the registrant as of, and for, the periods presented in this report;
4. The registrant's other certifying officer(s) and I are responsible for establishing and maintaining disclosure controls and procedures (as defined in Exchange Act Rules 13a-15(e) and 15d-15(e)) and internal control over financial reporting (as defined in Exchange Act Rules 13a-15(f) and 15d-15(f)) for the registrant and have:
 - (a) Designed such disclosure controls and procedures, or caused such disclosure controls and procedures to be designed under our supervision, to ensure that material information relating to the registrant, including its consolidated subsidiaries, is made known to us by others within those entities, particularly during the period in which this report is being prepared;
 - (b) Designed such internal control over financial reporting, or caused such internal control over financial reporting to be designed under our supervision, to provide reasonable assurance regarding the reliability of financial reporting and the preparation of financial statements for external purposes in accordance with generally accepted accounting principles;
 - (c) Evaluated the effectiveness of the registrant's disclosure controls and procedures and presented in this report our conclusions about the effectiveness of the disclosure controls and procedures, as of the end of the period covered by this report based on such evaluation; and
 - (d) Disclosed in this report any change in the registrant's internal control over financial reporting that occurred during the registrant's most recent fiscal quarter (the registrant's fourth fiscal quarter in the case of an annual report) that has materially affected, or is reasonably likely to materially affect, the registrant's internal control over financial reporting.
5. The registrant's other certifying officer(s) and I have disclosed, based on our most recent evaluation of internal control over financial reporting, to the registrant's auditors and the audit committee of the registrant's board of directors (or persons performing the equivalent functions):
 - (a) All significant deficiencies and material weaknesses in the design or operation of internal control over financial reporting which are reasonably likely to adversely affect the registrant's ability to record, process, summarize and report financial information; and
 - (b) Any fraud, whether or not material, that involves management or other employees who have a significant role in the registrant's internal control over financial reporting.

Date: November 7, 2006

/s/ JAN JOHANNESSEN
Jan Johannessen
Senior Vice President and Chief Financial Officer

**CERTIFICATION OF CHIEF EXECUTIVE OFFICER
PURSUANT TO 18 U.S.C. SECTION 1350, AS ADOPTED PURSUANT TO
SECTION 906 OF THE SARBANES-OXLEY ACT OF 2002**

In connection with the Quarterly Report of Lattice Semiconductor Corporation (the "Company") on Form 10-Q for the quarter ended September 30, 2006 (the "Report"), I, Stephen A. Skaggs, President and Chief Executive Officer of the Company, certify, pursuant to 18 U.S.C. Section 1350, as adopted pursuant to Section 906 of the Sarbanes-Oxley Act of 2002, that:

- (1) The Report fully complies with the requirements of Section 13(a) or 15(d) of the Securities Exchange Act of 1934; and
- (2) The information contained in the Report fairly presents, in all material respects, the financial condition and results of operations of the Company.

A signed original of this written statement has been provided to the Company and will be retained by the Company and furnished to the SEC or its staff upon request.

/s/ STEPHEN A. SKAGGS

Stephen A. Skaggs

President and Chief Executive Officer

Date: November 7, 2006

**CERTIFICATION OF CHIEF FINANCIAL OFFICER
PURSUANT TO 18 U.S.C. SECTION 1350, AS ADOPTED PURSUANT TO
SECTION 906 OF THE SARBANES-OXLEY ACT OF 2002**

In connection with the Quarterly Report of Lattice Semiconductor Corporation (the "Company") on Form 10-Q for the quarter ended September 30, 2006 (the "Report"), I, Jan Johannessen, Senior Vice President and Chief Financial Officer of the Company, certify, pursuant to 18 U.S.C. Section 1350, as adopted pursuant to Section 906 of the Sarbanes-Oxley Act of 2002, that:

- (1) The Report fully complies with the requirements of Section 13(a) or 15(d) of the Securities Exchange Act of 1934; and
- (2) The information contained in the Report fairly presents, in all material respects, the financial condition and results of operations of the Company.

A signed original of this written statement has been provided to the Company and will be retained by the Company and furnished to the SEC or its staff upon request.

/s/ JAN JOHANNESSEN

Jan Johannessen

Senior Vice President and Chief Financial Officer

Date: November 7, 2006
