



The Low Power Programmable Leader

Corporate Overview

November 2025

Safe Harbor Statement

Forward Looking Statements

This presentation may contain forward-looking statements within the meaning of the Private Securities Litigation Reform Act of 1995 and made pursuant to the safe harbor provisions of Section 27A of the Securities Act of 1933, as amended, and Section 21E of the Securities Exchange Act of 1934, as amended. These statements involve estimates, assumptions, risks and uncertainties. Any statements about our expectations, beliefs, plans, objectives, assumptions or future events or performance are neither historical facts nor assurances of future performance and may be forward-looking. Such forward-looking statements include, but are not limited to, statements relating to our revenue and EPS growth, gross margin projections, operating expense projections, our future financial performance and related drivers, and our expectations related to the general market. Other forward-looking statements may be indicated by words such as “will,” “could,” “should,” “would,” “may,” “expect,” “plan,” “project,” “anticipate,” “intend,” “forecast,” “future,” “believe,” “estimate,” “predict,” “propose,” “potential,” “continue” or the negative of these terms or other comparable terminology. Estimates of future revenue and other financial and operational outcomes are inherently uncertain due to factors such as global economic conditions which may affect customer demand, the cyclical nature of the semiconductor industry, pricing and inflationary pressures, competitive actions, international trade disputes and sanctions, the potential impact of global pandemics, the impact of tariffs, license requirements or similar actions on our suppliers, distributors, and customers, including the impact on the costs of our products, the products into which they are integrated, and the impact on demand due to costs and uncertainty; and other significant risks and uncertainties that are beyond our ability to predict or control. Actual gross margin percentage and operating expenses could vary from the estimates on the basis of, among other things, changes in revenue levels, changes in product pricing and mix, changes in wafer, assembly, test and other costs, variations in manufacturing yields, the failure to sustain operational improvements, and the actual amount of compensation charges due to stock price change, including those risks more fully described in in our filings with the Securities and Exchange Commission, including Item 1A in Lattice’s most recent Annual Report on Form 10-K, especially those under the captions “Risk Factors” and “Management’s Discussion and Analysis of Financial Condition and Results of Operations”, all of which are expressly incorporated herein by reference.

You should not unduly rely on forward-looking statements because actual results could differ materially from those expressed in any forward-looking statements. In addition, any forward-looking statement applies only as of the date on which it is made. We do not intend to, and undertake no obligation to, update or revise any forward-looking statements, whether as a result of events or circumstances after the date hereof or to reflect the occurrence of unanticipated events.

Use of non-GAAP Financial Information

To supplement the Company’s financial information presented on a GAAP basis, we have provided non-GAAP financial information in this presentation. Non-GAAP financial information is not meant as a substitute for GAAP results but is included because management uses such information to evaluate and manage the Company and believes such information is useful to our investors for informational and comparative purposes. These non-GAAP measures should be considered in addition to, and not as a substitute for, the results prepared in accordance with GAAP. See the appendices in our latest earnings report for reconciliation to the most comparable GAAP measure.

Trademarks – General Notice

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Agenda

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Company Overview

2

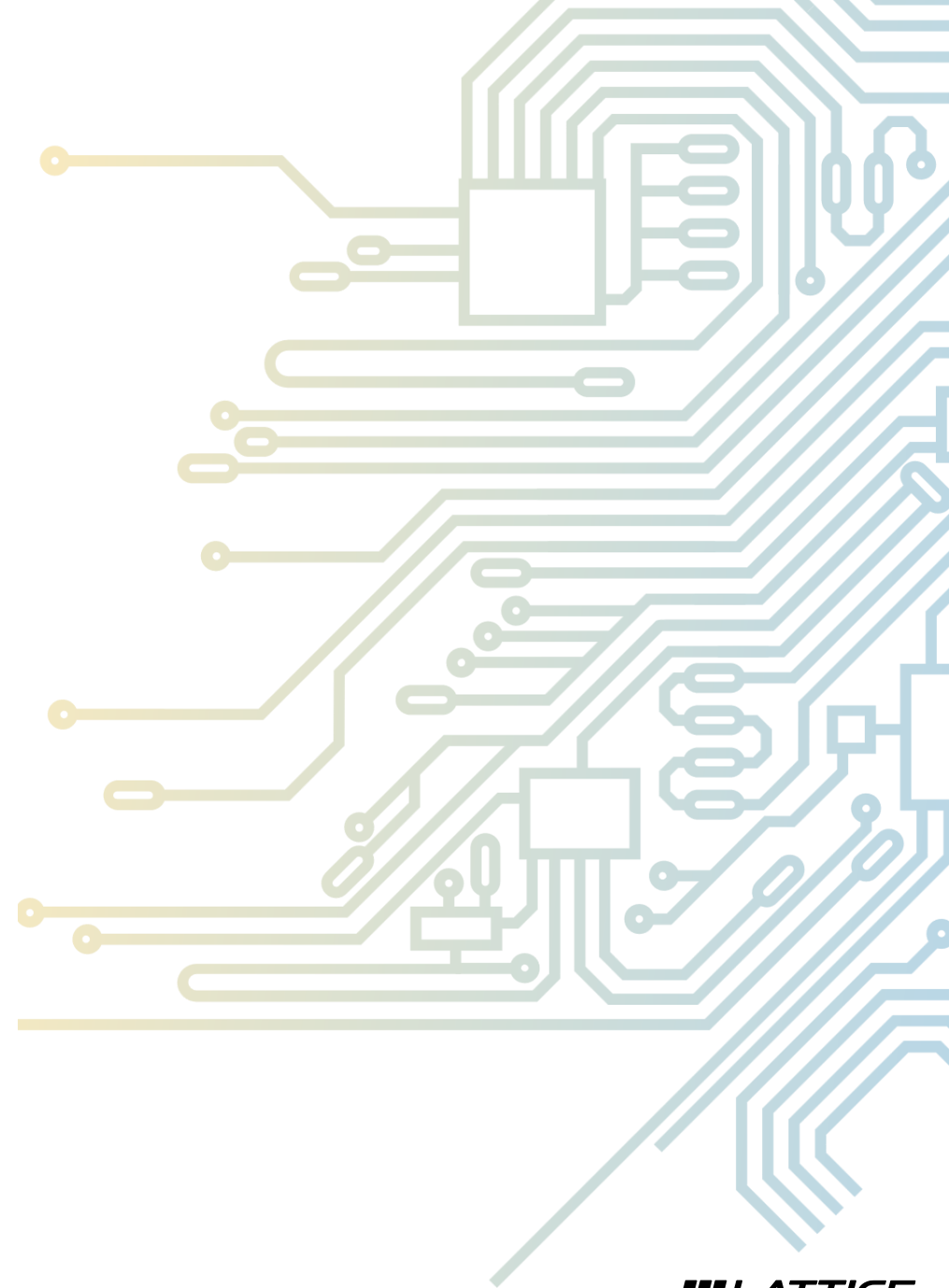
Products & Solutions

3

End Markets & Applications

4

Financials



Our Mission



To Be the Low Power Programmable Leader

DIFFERENTIATED

FPGA Platform
Optimized for Small
and Mid-range

DIVERSIFIED

Across Largest and
Fastest Growing
Applications

DELIVERING

Faster Than
Industry Growth

Lattice Semiconductor Overview

APPLICATIONS & MARKETS

We enable secure control, flexible connectivity, and low power compute acceleration



COMMUNICATIONS & COMPUTING

45%



INDUSTRIAL & AUTOMOTIVE

46%



CONSUMER

9%

WORLD CLASS SUPPLIER

#1

World's largest volume supplier of FPGA

Tier 1

Supplier with 40+ years of innovation



GROWING CUSTOMER BASE



GLOBAL SUPPORT



Holding Ourselves to the Highest Corporate Stewardship Standards

CULTURE OF INNOVATION



The Low Power Programmable Leader

ENVIRONMENTALLY CONSCIOUS



Operational Excellence | Supply Chain Management

INCLUSION & SOCIAL WELLBEING



Our People | Our Communities | Our Culture

TRANSPARENCY & INTEGRITY



Governance Principles | Ethical Standards | Continuous Improvement

GSA MOST RESPECTED PUBLIC COMPANY FIVE YEARS IN A ROW



STRONG & GROWING RECOGNITION FOR CLEANTECH PRODUCT INNOVATION



Lattice Executive Leadership Team



Ford Tamer
CEO



Pravin Desale
Research & Development




Esam Elashmawi
CSMO



Tracy Feanny
General Counsel




Lorenzo Flores
CFO



Divyesh Shah
Operations & Quality



Erhaan Shaikh
Sales



Nicole Singer
Human Resources

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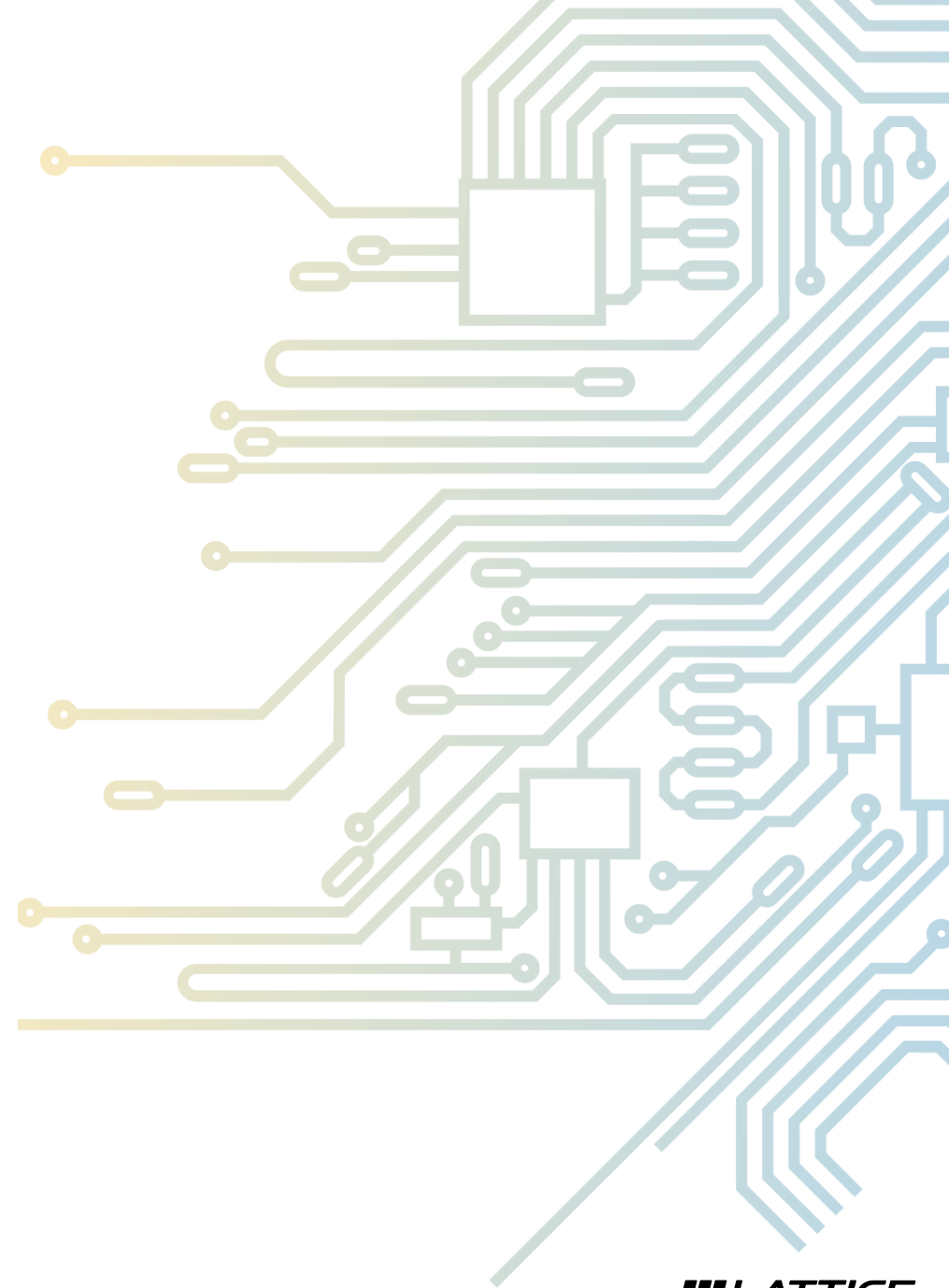
Products & Solutions

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Lattice Value Proposition



The Low Power Programmable Leader

Lowest
POWER

A yellow lightning bolt icon inside a circle, representing power.

Smallest
SIZE

A yellow microscope icon inside a circle, representing size.

EASE
of Use

A yellow hand cursor icon inside a circle, representing ease of use.

Lattice FPGA Portfolio

PLATFORM

DEVICE FAMILIES

LATTICE
AVANT™



Avant-E

Edge-optimized
Processing



Avant-G

Cutting-edge General
Purpose Processing



Avant-X

Advanced
Connectivity

LATTICE
NEXUS 2™



Certus-N2

ADVANCED GENERAL PURPOSE SMALL FPGAs

SYSTEM EXPANDABILITY	SECURE BRIDGING

LATTICE
NEXUS



CrossLink-NX

Embedded Vision
Processing



Certus-NX

General Purpose
Processing



Mach-NX

Next Gen Hardware
Security



CertusPro-NX

Advanced General
Purpose Processing



MachX05-NX

Enhanced System
Monitor and Control



MachX05T-NX

Advanced System
Control



CrossLinkU-NX

Embedded Vision
Processing with USB



MachX05-NX TDQ

Industry 1st
PQC-Enabled FPGA

FPGA Platform Leadership



Architected for applications requiring up to
16G SERDES and up to **200k LCs**



Architected for applications requiring up to
25G SERDES and up to **500k LCs**



**LOWER
POWER**



**FASTER
PERFORMANCE**



**SMALLER
SIZE**

Software Solution Stack Portfolio




LATTICE sensAI™

Low Power Edge AI

High Performance Inference Under 1W

Supports Industry Standard ML Frameworks

Complete Solution Enablement



LATTICE mVISION™

Low Power Embedded Vision

Flexible Image Sensor Bridging & Aggregation

Image Processing Integration

Complete Solution Enablement



LATTICE Sentri™

Cyber Resilient Root of Trust

Secure Hardware Creates Root-of-Trust for Systems

Cryptographically Secured Supply Chain

Protection Against Cloning, Counterfeiting, Trojan Insertion, & Simulation



LATTICE Automate™

Accelerating Factory Automation

Accelerates industrial automation development

Supports use cases like motor control, real-time networking, & predictive maintenance

Complete solution enablement



LATTICE ORAN™

Enabling ORAN Deployment

Enables zero trust security and data protection in networks

Flexible, Tight Fronthaul Synchronization

Acceleration with Low Power



LATTICE Drive™

Adaptable Automotive Design

DisplayPort connectivity

Video scaling up to 4K

Local dimming for contrast enhancement

Bridging & networking

Easy-to-use Software



**LATTICE
DIAMOND™
DESIGN SOFTWARE**

Powerful FPGA Design & Verification Environment

- Easy Design Exploration
- Easy to Use Powerful Tools
- Optimized for Lattice Devices

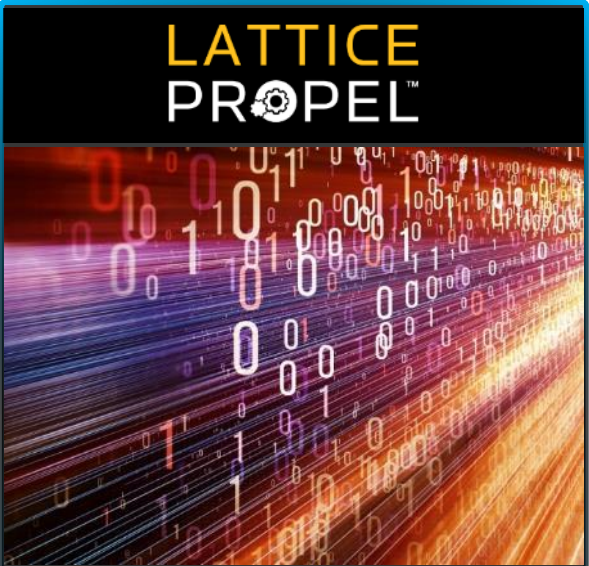


**LATTICE
RADIANT™
DESIGN SOFTWARE**

```
if_operation == "Mirror_Y")  
    mirror_mod.use_x = False  
    mirror_mod.use_y = True  
    mirror_mod.use_z = False  
elif_operation == "Mirror_Z")  
    mirror_mod.use_x = False  
    mirror_mod.use_y = False  
    mirror_mod.use_z = True  
  
Selection at the end - add back the deselected mirr  
error_ob.select-1  
modifier_ob.select-1  
py.context.scene.objects.active = modifier_ob
```

Best-in-class, Easy-to-use Design Software


- Simplified Flow for Faster Design
- Increase Re-use with IP Tools
- Leading Synthesis & Simulation



**LATTICE
PROPEL™**

Complete Toolset for Embedded System Design

- IP System Integration Environment
- Software Development Kit & Libraries
- Build, Compile, Analyze, Debug



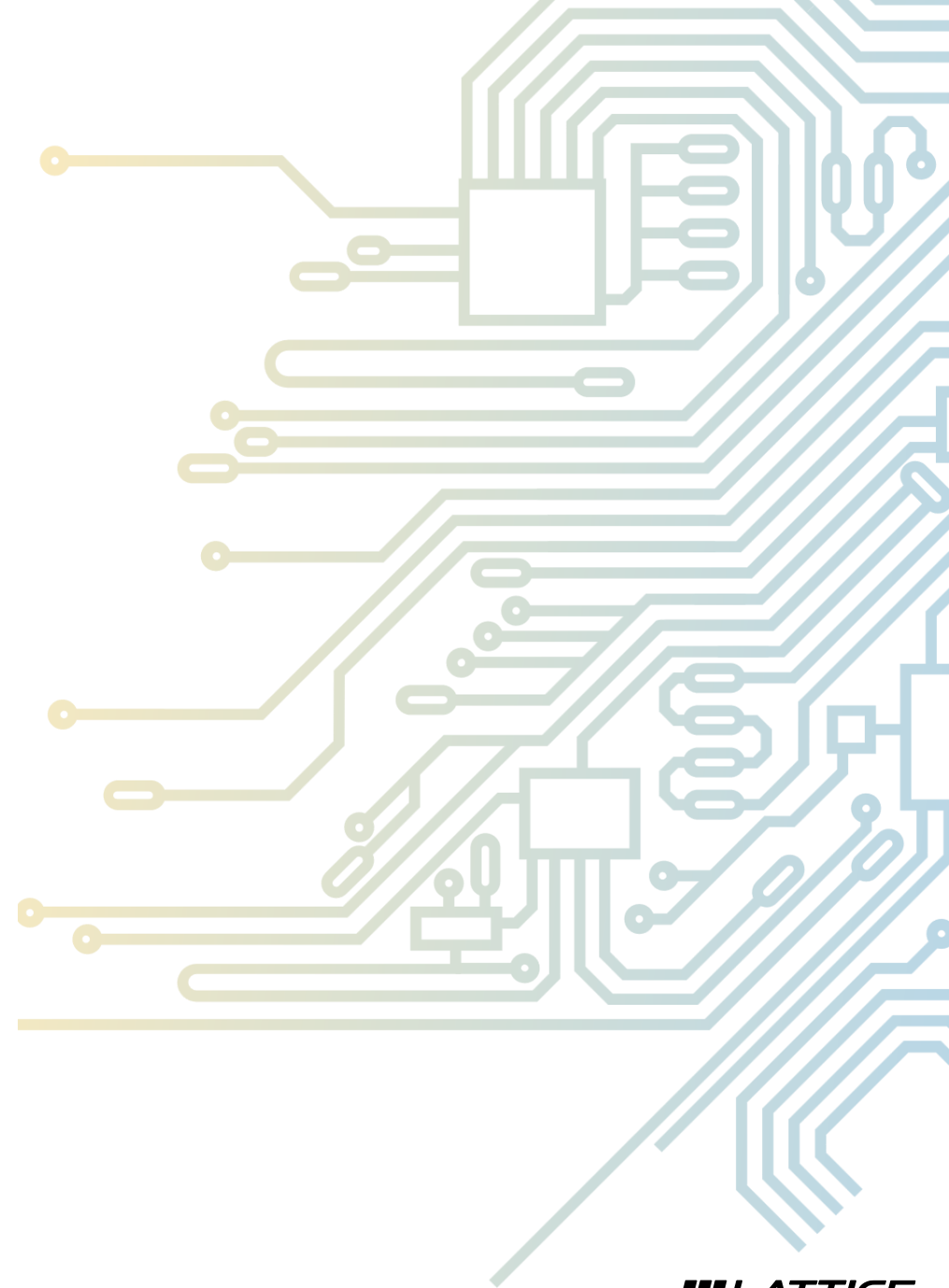
Glance
by MIRAMETRIX®

Advanced Computer Vision Software for the Edge

- Security and Privacy Protections
- Digital Wellbeing Capabilities
- Facilitates Intelligent Collaboration & Productivity

Agenda

- 1 Company Overview
- 2 Products & Solutions
- 3 End Markets & Applications**
- 4 Financials



Innovation Leadership From Edge to Cloud

DATACENTER



aws DELL Technologies Google H3C
 Hewlett Packard Enterprise Lenovo Meta
 Microsoft NetApp SUPERMICR

Top 10 Server & Storage Providers

COMMUNICATIONS



Adtran ARISTA ciena
 CISCO ERICSSON FUJITSU JUNIPER NETWORKS
 NEC NOKIA SAMSUNG

10 Leading Comms OEMs


INDUSTRIAL



ABB EMERSON GE Honeywell
 MITSUBISHI ELECTRIC OMRON Panasonic
 Rockwell Automation Schneider Electric SIEMENS

Top 10 Factory Automation Leaders

AUTOMOTIVE



BMW BYD Ford gm
 LUCID Maserati
 mazda Mercedes-Benz TESLA TOYOTA

10 Leading Auto OEMs

CLIENT



ASUS DELL hp
 Lenovo LG

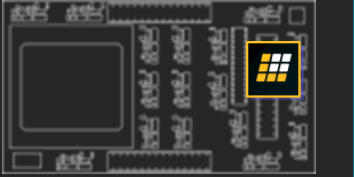
Top 5 PC OEMs - >50MU Shipped

Lattice is the #1 Supplier for Small FPGAs Worldwide

Lattice Solves Datacenter Challenges

HARDWARE ACCELERATION

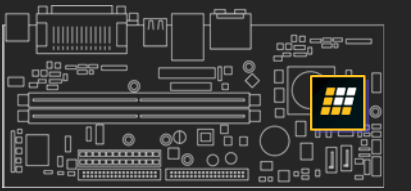
Offload
Attestation
Reporting



A schematic diagram of a hardware acceleration module, showing various components and a Lattice logo in the top right corner.

STORAGE CARD

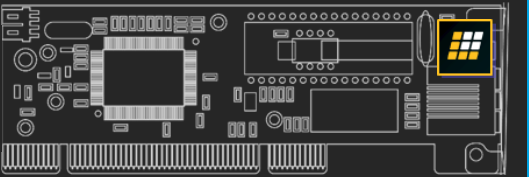
Key Mgmt
Hot Swap
Hot Plug



A schematic diagram of a storage card module, showing various components and a Lattice logo in the top right corner.

GPU CARD

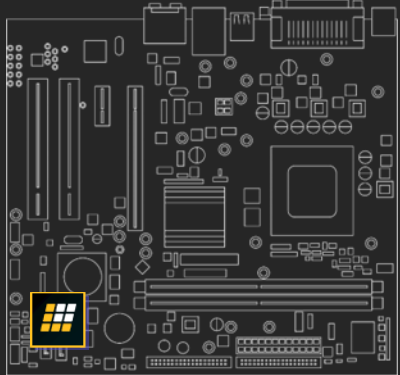
Power Control
Reporting
Throttling



A schematic diagram of a GPU card module, showing various components and a Lattice logo in the top right corner.

MOTHERBOARD

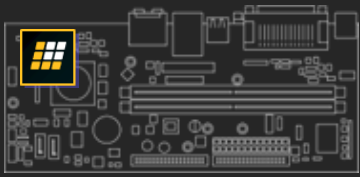
(CPU Agnostic)



A schematic diagram of a motherboard module, showing various components and a Lattice logo in the bottom left corner.

Bridging
I/O Expansion
Board Control
Power Sequence
Signal Aggregation
Glue Logic
Fan Control
Re-Timer

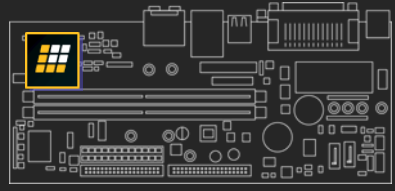
SECURE CONTROL MODULE



A schematic diagram of a secure control module, showing various components and a Lattice logo in the top left corner.

BMC
PROT / PFR
Attestation

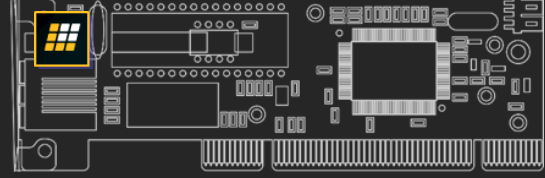
NETWORK CARD



A schematic diagram of a network card module, showing various components and a Lattice logo in the top left corner.

Power Control
Reporting
Offload

ADD-IN CARDS



A schematic diagram of an add-in cards module, showing various components and a Lattice logo in the top left corner.

Control
Bridging
Aggregation

Lattice Solves Communications Challenges



Lattice Solves Industrial Challenges



Smart Factory

- Collision Avoidance
- Edge Computing
- Functional Safety
- Industrial Networking
- Machine Vision
- Motor Control
- Predictive Maintenance
- Programmable Logic Control
- Object Identification
- Sensor Fusion
- Robotics



Test & Measurement

- High-Speed Data Acquisition
- Signal Processing
- Emulation and Validation
- Pattern Generation and Analysis
- Timing Analysis
- Error Detection and Correction
- Jitter and Noise Measurement
- Power Analysis
- Temperature and Stress Testing
- Portables and Handhelds



Medical

- Digital Endoscopy Systems
- MRI and CT Image Processing
- Ultrasound Signal Processing
- Electrocardiogram Signal Processing
- X-ray Processing
- Blood Analysis Equipment
- Health Monitors
- Robotic Surgery Assistants
- Secure Medical Data Processing
- Patient Monitoring Systems



Aerospace & Defense

- Radar Signal Processing
- Avionics Control Systems
- Digital Beamforming
- Satellite Communications
- GPS and Navigation Systems
- Infrared and Optical Image Processing
- Ruggedized Systems for Harsh Environments
- Secure Communications



Broadcast / ProAV

- Video Encoding/Decoding
- Live Video Streaming
- High-Resolution Video Processing
- Video Scaling and De-interlacing
- Color Correction and Enhancement
- Image Stabilization
- Audio Processing and Mixing
- Multi-Protocol Bridging
- Real-Time Graphics Overlays
- Low-Latency Switching

Lattice Solves Human Machine Interface Challenges



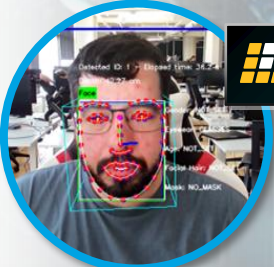
Gesture Detection



Person Detection



Attention Sensing



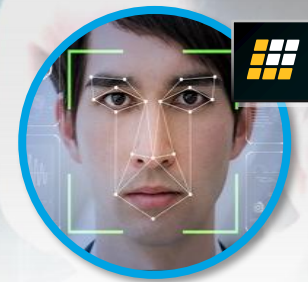
Enhanced Security



Driver Monitoring



Audio/Visual User ID



Lattice Solves Automotive Challenges

Infotainment

- Display Bridging
- Local Dimming
- Display Safety
- Daylight Enhancement
- ISP

ADAS

- E-Mirror/CMS
- Thermal Camera
- Radar Sensor Bridging & Aggregation
- Lidar Sensor Bridging & Aggregation

Networking

- Zonal / Central Gateway
- Network Bridge

Electric Powertrain

- Inverter / Charger
- Battery Management

Edge AI

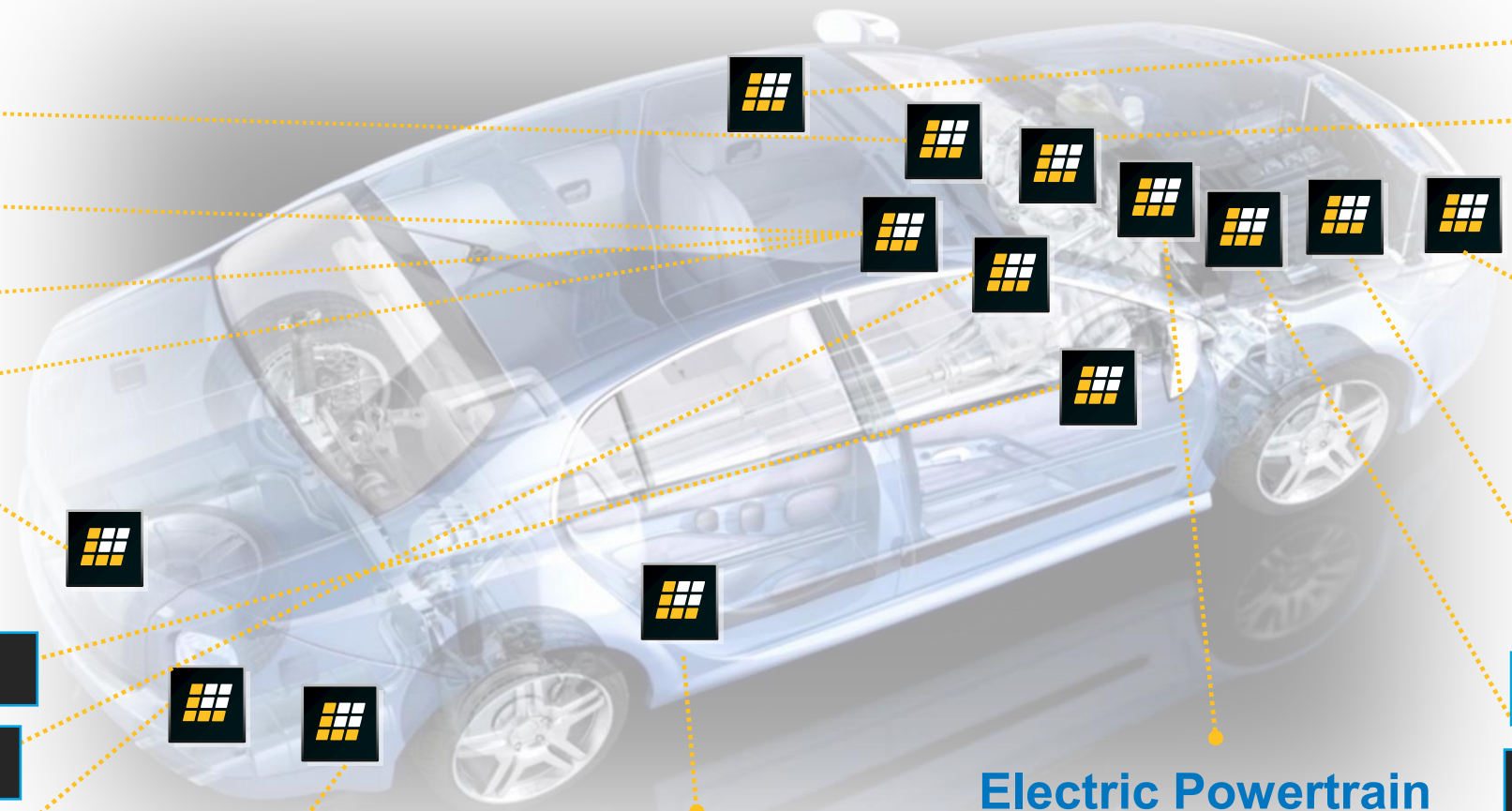
- DMS / OMS
- Sentry

Lighting

- Smart Lamps

Security

- PQC RoT
- Platform Security



Lattice Solves Consumer Challenges



Lattice Drives AI Innovation

Gen AI Servers

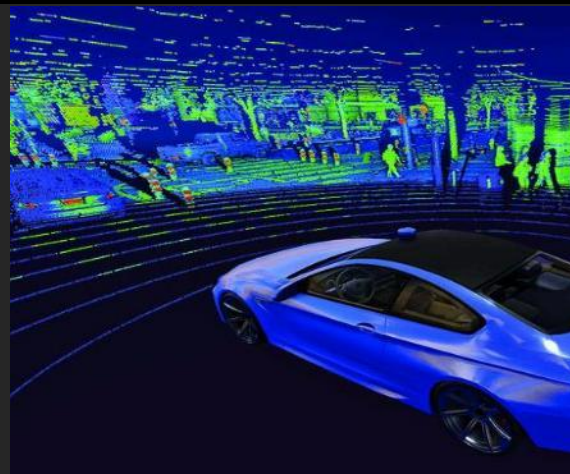


Leading Control and Security in AI Servers

Up to >50 FPGAs per server rack

Deployed at majority of Hyperscalers and OEM/ODMs

Sensor Proliferation

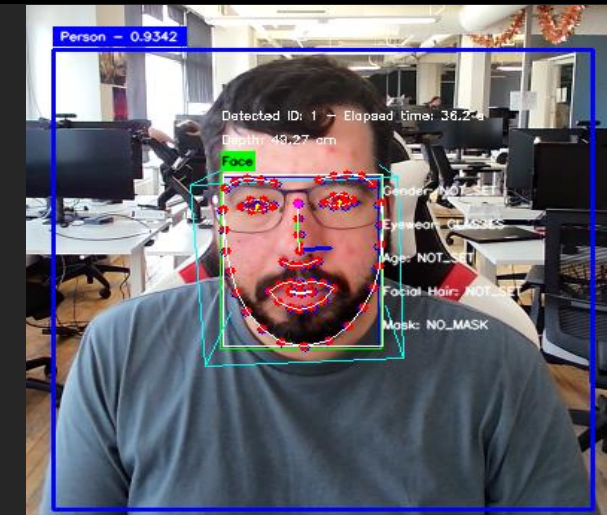


Data Fusion and Optimized Streaming

Partnership with Nvidia to simplify sensor connectivity

Diverse sensor connectivity in autonomous systems

Intelligent Edge



Human Machine Interfacing

Advanced computer vision experiences enabling security, privacy, safety, and wellbeing

Shipped 40M+ units at top PC OEM; Expanding in Adjacent Markets

Enabling AI from hybrid cloud to the intelligent edge with low power FPGAs and software

Lattice Drives Vision Innovation

Robotics



Sensor Streaming & Processing in Robotics

Camera, Radar, and Lidar Bridging and Aggregation

Synchronization and Real-Time Low Power Edge Processing

ADAS & Infotainment



ADAS and Display Bridging & Processing

Camera and Sensor Streaming; Power Optimized Processing

Display Connectivity and Video Quality Enhancement

Streaming Media



Machine Vision & Video Transmission

Low Latency High Performance Machine Vision and Control

Networked Video Transmission Across Enterprise and WAN

Enabling Autonomous Machines and Rich Media with Low Power FPGAs and Software

Lattice Drives Security Innovation

Strong RoTs



Unique FPGA Based Hardware Roots of Trust

Integrated Lockable Dual-Boot
Flash – Undeniable Service

Hardened NIST Qualified
Cryptographic Algorithms

Cyber Resiliency

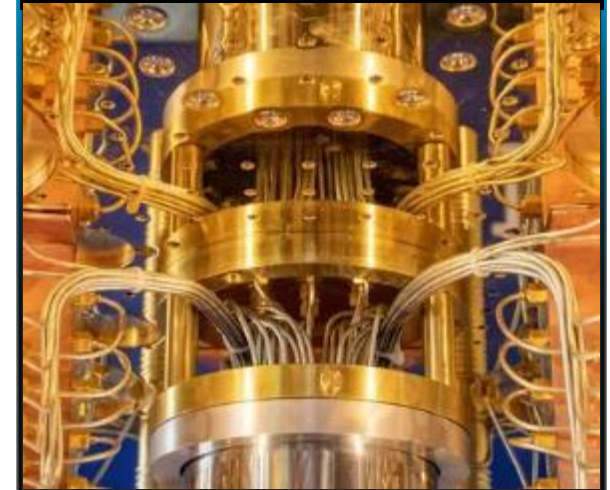


Cyber Resilient Pioneers

Processor Independent Platform
Firmware Resiliency (PFR)

Cyber Resilience Act (CRA)
Ready

Post Quantum



Post Quantum Crypto (PQC) Agility

PQC Ready With Latest NIST
Approved PQC Algorithms

Crypto-Agility For In-field Updates
& Upgrades as PQC Evolves

Enabling Next Generation Dynamic Security – Multi-channel Real-time System Protections

Agenda

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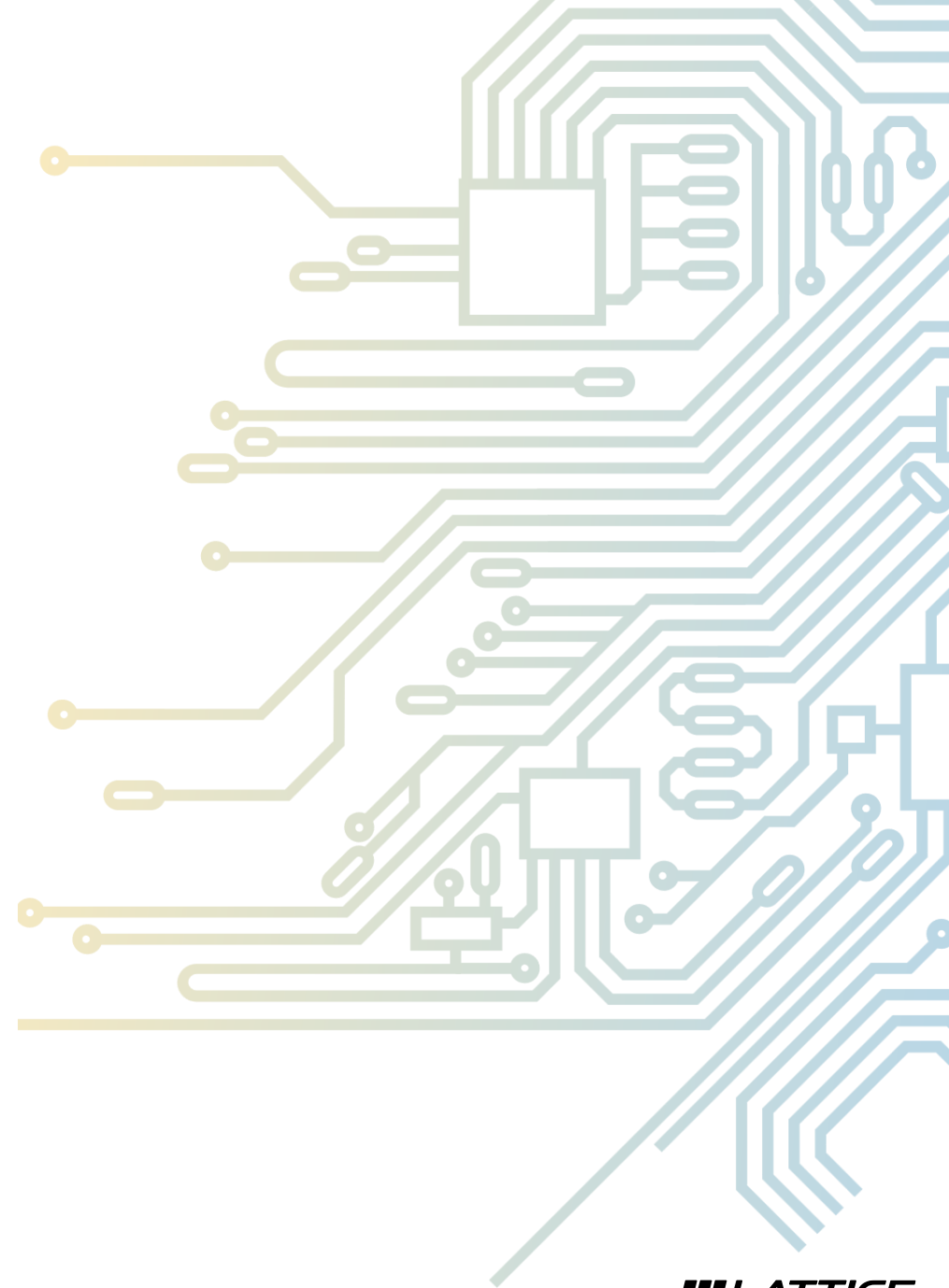
Products & Solutions

3

End Markets & Applications

4

Financials



Q3 2025 Earnings Overview & Highlights

REVENUE

\$133.3M

7.6% Growth QoQ

GROSS MARGIN

69.5%*

ADJUSTED EBITDA

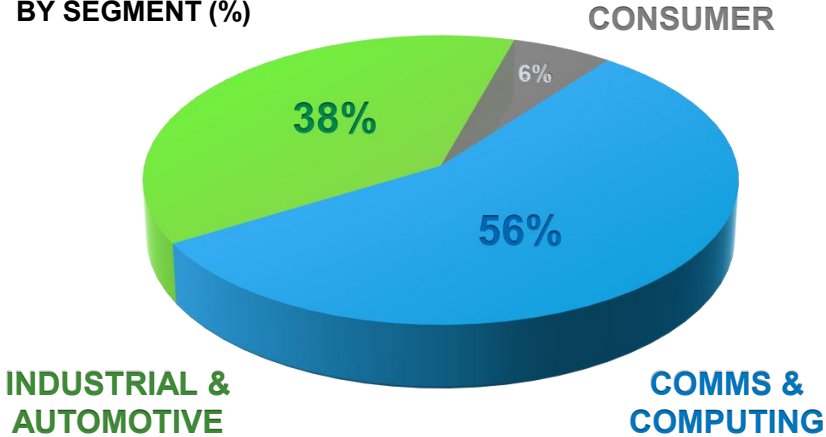
35.6%*

"We delivered a strong quarter, with broad-based growth across our end markets, and grew non-GAAP earnings 17% quarter over quarter. Our Communications and Computing business achieved record revenue, and we are expecting continued growth into the fourth quarter and beyond. We continue to drive operating leverage and expand profitability, with significant revenue and non-GAAP EPS growth of 13% and 29%, respectively, expected in the second half of 2025 compared to the first half of 2025."

Ford Tamer, CEO

End Market Overview

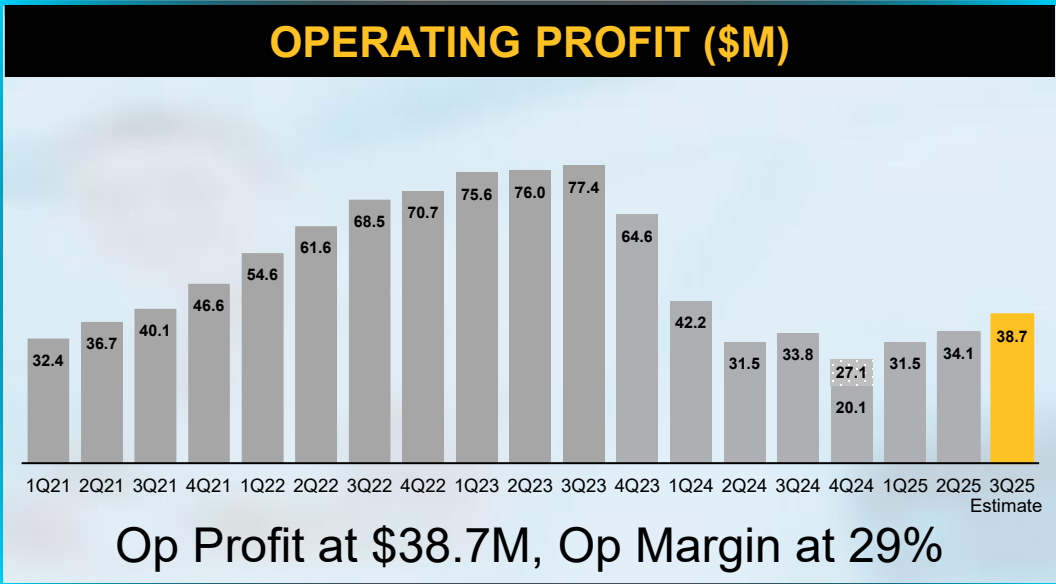
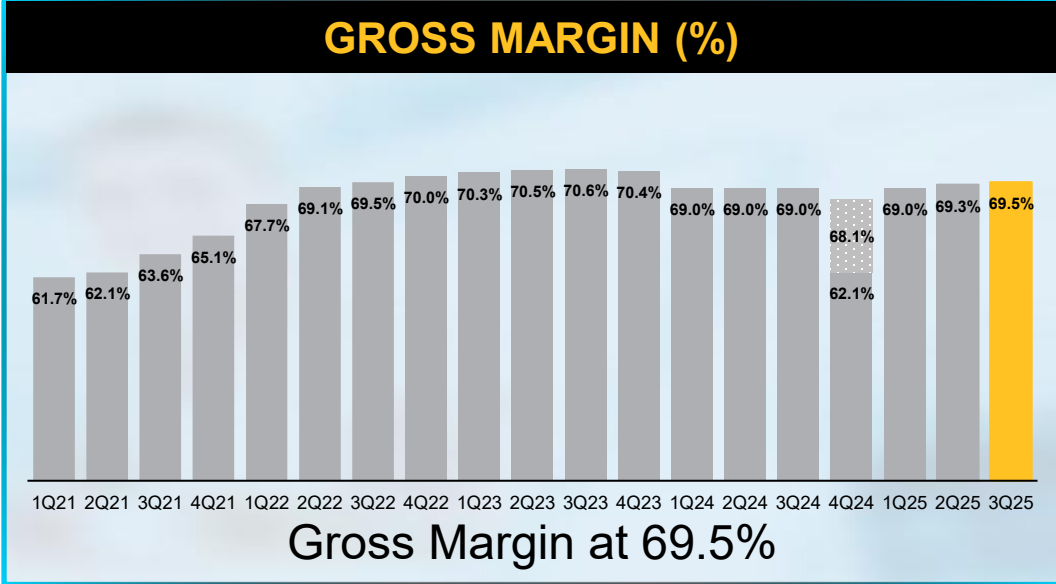
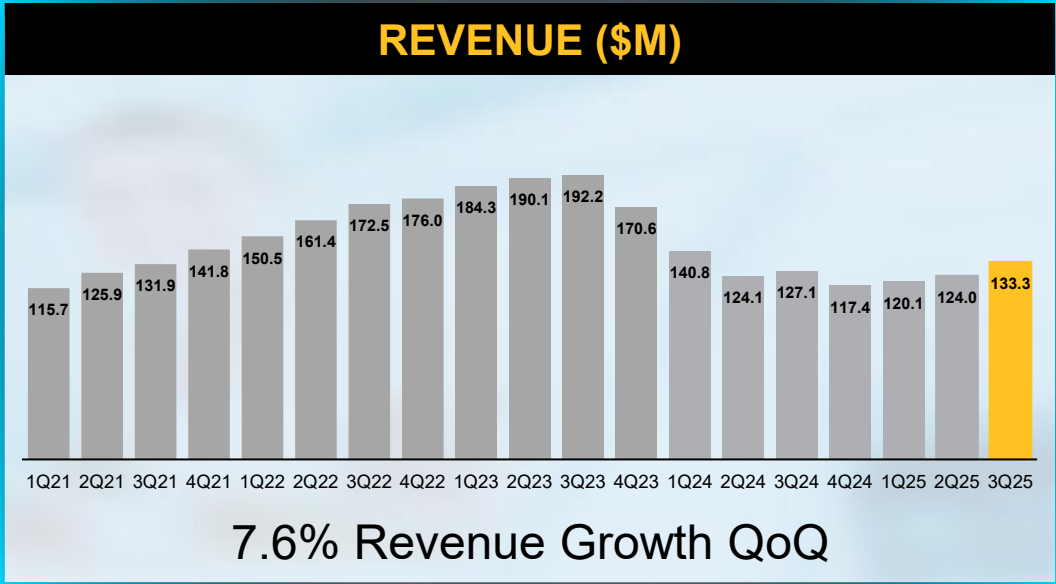
Q3'25 REVENUE BY SEGMENT (%)



Highlights

- Grew Q3'25 revenue, gross margin, and profitability sequentially; Highest sequential revenue growth in more than four years; Guiding both revenue and profitability up in Q4
- Record Communications & Computing segment revenue in Q3, up 21% YoY, with an expanding footprint in general purpose and AI servers
- Percentage of AI usage across products expected to be in the high teens in 2025 and in the mid-20's in 2026
- Launched the Lattice MachXO5™ NX TDQ family, industry's 1st PQC-ready FPGA
- Industrial and Automotive channel inventory normalization remains on track for year-end
- Remain on track to hit goal of high-teens percentage of new product revenue for full year 2025

Q3 2025 Financial Results





The Low Power Programmable Leader