Lattice Semiconductor Showcases Easy-to-Use Firmware Security Solution at 2020 OCP Summit

February 20, 2020

HILLSBORO, Ore.--(BUSINESS WIRE)--Feb. 20, 2020-- Lattice Semiconductor (NASDAQ: LSCC), the low power programmable leader, today announced the company will attend the 2020 Open Compute Project Global Summit to demonstrate the company's flexible, scalable, and customizable Platform Firmware Resiliency (PFR) solution based on its MachXO3D™ FPGA for secure system control.The Open Compute Project (OCP) is a rapidly growing, global community focused on redesigning hardware technology to efficiently support the growing demands on the compute infrastructure.

Firmware vulnerabilities can expose OEMs to problems like data and design theft, product cloning and overbuilding, and device tampering or hijacking. The US National Institute of Standards and Technology (NIST) Platform Firmware Resiliency standard provides guidelines for detecting, protecting, and recovering from unauthorized changes to system firmware. MachXO3D FPGAs are the industry's first control-oriented FPGAs compliant with the PFR specification and provide a quick and easy way for developers to design PFR-protected servers. Acting as a hardware Root of Trust (RoT), the MachXO3D device can block firmware attacks during system operation and ensures that all processors in the system only execute trusted firmware. In addition to the MachXO3D FPGA, the Lattice PFR solution includes software tools and IP that enable OEMs to easily adopt and customize PFR functionality for their servers through simple modifications to the embedded C code.

"When used as the control PLD to manage power supplies and other hardware control signals in servers, a MachXO3D FPGA is typically configured to be the first on/last off device when the server powers up or down," said Shyam Chandra, Business Development Manager, Lattice Semiconductor. "This makes the MachXO3D an ideal device for integrating hardware Root of Trust functionality, a key requirement of the NIST PFR specification that enables servers to defend against illegitimate attempts to access any firmware instances in the system."

Who: Lattice Semiconductor

What: 2020 OCP Global Summit

When: March 4, 2020 (11 AM – 7:30 PM) and March 5, 2020 (10 AM - 5 PM)

Where: San Jose Convention Center

Booth A-31

W. 150 San Carlos St.

San Jose, California 95113

For more information about the Lattice PFR solution, please visit https://www.latticesemi.com/en/Solutions/Solutions/SolutionsDetails02/PFR.

For more information about the MachXO3D family of FPGAs, please visit http://www.latticesemi.com/MachXO3D.

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing communications, computing, industrial, automotive and consumer markets. Our technology, long-standing relationships, and commitment to world-class support lets our customers quickly and easily unleash their innovation to create a smart, secure and connected world.

For more information about Lattice, please visit www.latticesemi.com. You can also follow us via LinkedIn, Twitter, Facebook, YouTube, WeChat, Weibo or Youku.

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